

**A DENOTATIONAL SEMANTICS FOR SHARED-MEMORY
PARALLELISM AND NONDETERMINISM**

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SHARED-MEMORY PARALLELISM AND NONDETERMINISM***

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Abstract

It is first shown how to construct a continuation from a deterministic Vienna Definition Language control tree. This construction is then applied to nondeterministic control trees. The result is a denotational but not quite continuation semantics for arbitrary shared-memory nondeterminism and parallelism. The implications of this result are discussed.

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1. Introduction

1.1 What is done

This paper describes the development of a denotational semantics for nondeterminism and parallelism in a shared memory. The denotation of a program fragment is a tree representing a partial ordering of actions to be performed in the rest of the computation. Forking in the tree represents spawning of processes. Each action in the tree causes either an indivisible state change or a macro expansion to a subtree of other actions to be performed. Thus, this denotation is similar to Plotkin's resumptions [Plo76, Smy78]. All such trees of actions are finite even if the program gives rise to nonterminating processes, i.e., macro expansion to subtrees occurs only at run-time as needed.

It is convenient and advantageous to use VDL control trees as the tree denotation described above. It is convenient, because all the required mechanisms have already been set up [LLS70, LW69]. It is advantageous, because, as it is shown, given any VDL definition of a block structured language which is in a certain generally achievable normal form, it is possible to convert this interpreter definition into a denotational semantics of the same language giving the control trees as the meaning of program fragments.

In addition, it is shown that each control tree is an encoding of a function, called a P-continuation, which is on the value and storage returned by an instruction to the set of all answers obtained from all possible computations starting with the given instruction. It turns out the the function encoded by a control tree cannot be used in place of the control tree in the semantic equations, because information about possible interleavings has been lost.

Plotkin and later Smyth [Plo76, Smy78] present the power domain construction of resumptions R , where

$$R = S - P[S + (S \times R)]$$

where S is a domain of states. These resumptions may be taken as the denotation of program fragments giving rise to processes which access shared memory. Plotkin and Smyth observe that functions on states to sets of states are not powerful enough to model arbitrary interleaving.

Schwarz presents a power domain semantics to handle an expression (without assignment) language with features that require parallelism [Sch79].

Francez, Hoare, Lehmann, and de Roever [FHLR79] present a semantics of CSP, which has its processes communicate via message passing rather than through shared memory. This restriction in means of interaction between processes allows use of domains simpler than power domains. They ascribe to each process description a potentially infinite history tree with potentially infinite branching which denotes all potential communications with other processes. The meaning of a collection of processes is obtained by binding their history trees, i.e., by matching in pairs of history trees potential communications that represent actual communications between the processes owning the trees. The result of the binding process is not in the same domain of history trees, so it cannot be further bound.

A later work by Francez, Lehmann, and Pnueli [FLP80] improves on this last work by offering as the semantics of a process description a relation between sets of attainable states and the communication sequence needed to attain these states. This sequence, being linear, is simpler than the potentially infinitely branching history trees of the previous work. They provide a binding operator which matches and merges two or more processes' communication sequences to obtain the semantics of the combined processes in the

same domain. Thus the result of a binding can be further bound. They remark that to be able to handle arbitrary interleaved access to shared memory instead of just message passing would require the use of more complex power domain constructions.

Stoughton [Sto81] uses a mapping from states to sets of potentially infinite computation sequences as the meaning of a program or system. Interestingly, he programs the system's scheduler into his equations so that the scheduler can be varied from definition to definition.

The contribution of this paper is a denotational semantics which

1. is powerful enough to model arbitrary interleaved access to shared memory,
2. is finite,
3. is systematically constructible from a VDL interpreter definition of the same language, and
4. is an encoding for an easily constructed function on states to sets of states.

This last contribution is important because a definition is not considered abstract enough unless the meaning of a program is a function on states to states or sets of states.

1.2 History of this Paper

The history of this paper is instructive. In my graduate class on operational semantics, I teach the Vienna Definition Language (VDL) [LW69, LLS70, Weg72] as a language well-suited for writing operational definitions of programming languages. We cover several example definitions as well as how to convert a collection of instruction definitions into the state transition function they represent. The examples exhibit deterministic, nondeterministic, and parallel computations.

In the 1979 version of the course I decided to teach denotational semantics [Ten76, Ten78, Gor79] as well after having presented VDL. The lecture leading into the denotational semantics attempted to motivate the continuation idea by constructing a continuation from a deterministic control tree. Once the construction was understood, the functionalities of the continuation and of the semantic meaning function were obvious. Then it was easy to explain and understand denotational continuation semantics.

The construction had proceeded from deterministic control trees because I knew that nondeterminism had defied denotational, continuation semantics treatment [Gor79, ADA79]. In any case, after the class was over, just for the heck of it, I tried applying the construction also to nondeterministic control trees -- it seems to have worked.

1.3 Outline

This paper assumes at the outset familiarity with denotational, continuation semantics as described by Tennent or Gordon [Ten76, Ten78, Gor79]. The second Section, based on the lecture mentioned above, informally describes the construction of a continuation from a deterministic control tree. In order that a full knowledge of VDL is not needed to appreciate the basis of the construction, the discussion resorts to informal pictorial descriptions of VDL machine behavior. However, an acquaintance with the informal description of VDL and of the EPL machine given in [LLS70] or [Weg72] is helpful.

The third Section formalizes Section 1's construction of a continuation from a control tree. Because of the terseness of this Section, familiarity with the definition of VDL in [LLS70] or [LW69] helps. However, the formal development is self-contained.

The fourth Section returns to an informal discussion to apply a similar construction to nondeterministic control trees to obtain what is called a P-continuation (parallel continuation).

The fifth Section formalizes this new construction. This development assumes the construction of the third Section, and with that Section, is self-contained.

Finally, the sixth Section evaluates what has been done and attempts to clarify its relationship to the various existing kinds of semantics.

This paper uses the original, perhaps antiquated, VDL notation because it is talking about the original VDL and is showing that, in fact, the more modern presentations of semantics say exactly the same things. In addition, it is best to use the original VDL notation when talking about VDL so that yet another notation does not have to be developed.

2. From Deterministic Control Trees to Continuations (Informal Discussion)

2.1 Computations and Meanings

VDL is used to write information structure models [Weg70] (ISMs). An ISM is an abstract machine formally described as a triple*,

$$(is\text{-}state, is\text{-}initial\text{-}state, \Lambda),$$

the set of possible states, the set of initial states, and a state transition function Λ . In these first two Sections, all machines are deterministic so Λ applied to a state either yields the next state or is undefined indicating that the given state is a final state.

$$\Lambda: is\text{-}state \rightarrow is\text{-}state + \{undefined\}.$$

A computation is a possibly infinite sequence of states

$$\langle \xi_0, \xi_1, \dots, \xi_{i-1}, \xi_i, \dots \rangle$$

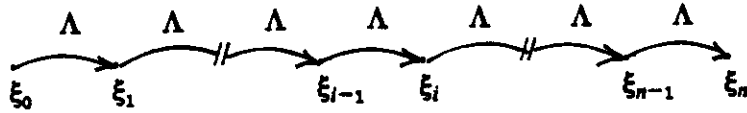
such that

- a. ξ_0 is an initial state,
- b. for each ξ_i with $i > 0$,

$$\xi_i = \Lambda(\xi_{i-1}),$$
- c. the sequence is not a proper initial subsequence of another sequence satisfying a) and b). (This condition guarantees that a finite sequence ends with a state ξ_n such that $\Lambda(\xi_n) = \text{undefined}$.)

If in the sequence, there is a state ξ_n such that $\Lambda(\xi_n) = \text{undefined}$, then the computation halts. Diagrammatically a finite computation may be shown as:

*Recall that in VDL, sets are defined by predicates. The predicate p is used to denote the set $\{x | p(x)\}$. Given a predicate p , $\hat{p} = \{x | p(x)\}$.



$\text{is-state}(\xi_i)$, for all $i \geq 0$
 $\text{is-initial-state}(\xi_0)$
 $\Lambda(\xi_{i-1}) = \xi_i$, for all $i > 0$
 $\Lambda(\xi_n) = \text{undefined}$

Denotational semantics has always tried to take as the meaning of a program the function it computes from the initial to the final state if it exists. That is, the meaning of a program p is a function f such that

$$f(\xi_0) = \begin{cases} \xi & \text{if the computation of } p \text{ from the initial state} \\ & \xi_0 \text{ halts at } \xi, \\ \perp & \text{if the computation of } p \text{ from the initial state} \\ & \xi_0 \text{ does not halt.} \end{cases}$$

One of the goals of this paper is to show the construction of this f from the state transition function Λ .

In addition, whatever the semantics, denotational semantics constructs meaning in syntax-directed manner; that is, the meaning of a construct is constructed from that of its direct syntactic components. It is typical that the meanings of all constructs, programs, assignment statements, single tokens, etc. are of the same type, e.g., function from start state to end state if it exists. Thus, viewing the abstract machine from a denotational framework, the meaning of a particular program construction, e.g., an assignment statement, is the composition of Λ across the states from the beginning to the end of the execution of the construct. Diagrammatically, the assignment $i := i + 1$ may be viewed in the context of a whole computation as shown in Figure 1.

Note the denotational idea of syntax-directed composition of Λ . The meaning of $i := i + 1$ is constructed from the meanings of i , $:=$, and $i + 1$. The meaning of $i := i + 1$ contributes to the construction of the meaning of the statement list containing $i := i + 1$. All of these meanings are of the same functionality, i.e., state to state.

The next Subsection begins the construction of these program and construct meaning functions in detail using an extension of the VDL EPL machine described in [LLS70] and also in [Weg72]. EPL is a simple block structured language with

1. integer and logical variables that must be declared in blocks,
2. potentially recursive procedures and functions with typeless, by-reference formal parameters, and
3. assignment, conditional, procedure call, and nested block statements.

The extension, called EPL+, adds label values, label variables, gotos, and while loops. The label and goto extensions appeared first in an unpublished report [BW72]. Appendix I contains the complete definition of EPL+.

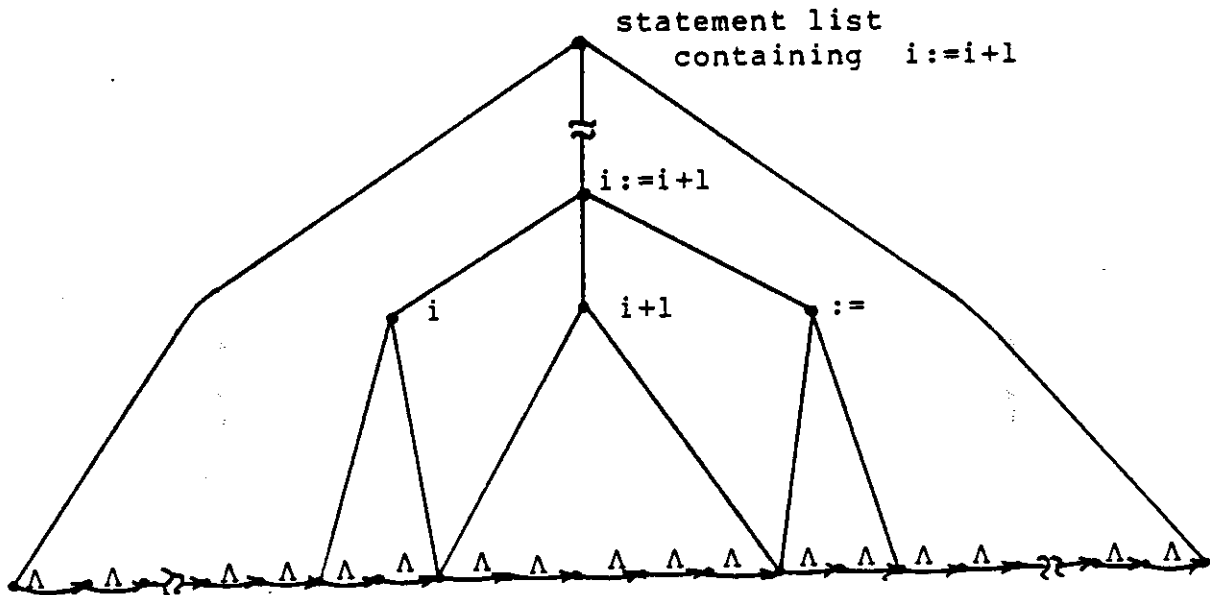


Figure 1

2.2 Modifications to EPL+ Machine

First, the EPL+ machine is given a deterministic control tree by getting rid of all forking in the control tree. This is done by

1. changing all commas in control tree representations into semicolons,
2. changing all macros which expand into sets of instructions, e.g.,

```
int-decl-part(t)=
  null;
  {int-decl(id(s-env(ξ),id(t))|id(t)≠Ω}
```

into sequentially recursing macros, e.g.,

```
int-decl-part(t)=
  is-<>(t)-null;
  T-int-decl-part(tail(t));
  int-decl(s-id(head(t))(s-env(ξ)),
  s-attr(head(t)))
```

This change may, in some cases, necessitate a change in the syntax of the program. An object which is a set of things, e.g., an object satisfying the predicate `is-decl-part`, may have to be changed into a list of things conveying the same information, e.g., an object satisfying the predicate `is-decl-list`, in order to permit sequencing through the things.

Second, the state is reorganized so that it has only a storage, an environment and a control tree (which is deterministic) i.e.,

$$is-state = (\langle s-stg:is-stg \rangle, \langle s-env:is-env \rangle, \langle s-c:is-c \rangle),$$

or to use more conventional denotational semantics notation

$$is-state = S \times U \times Cont.$$

It is necessary in this change to account for the information of the missing components, the unique name counter, the attribute and denotation directories, and the dump, in order to insure that correctness has been preserved.

The unique name counter is dispensed with by rewriting the `un-name` instruction to return any n such that $nos-stg(\xi) = \Omega$. In order to distinguish unallocated locations for which $nos-stg(\xi) = \Omega$ from allocated but uninitialized locations, the latter will have the value UNINIT.

The information normally found in the attribute and denotation directories is moved into the storage.

$$is-stg = (\{ \langle n: (\langle s-dn:is-dn \rangle, \langle s-st:is-type \rangle) \rangle \mid is-n(n) \})$$

$$is-dn = is-proc-dn \text{ Vis-funct-dn Vis-value Vis-label-dn Vis-UNINIT}$$

Since the dump saves the environment and control of the calling block or procedure instance, to get rid of the dump it is necessary to reorganize the block, procedure, and function entry and exit:

1. Instead of dumping the current control tree and then overwriting the current control tree with a new one, the new one is macro-appended to the current one.
2. The environment which is saved in the dump to be restored by `exit` is now made an actual parameter of the `exit`, which restores it from its formal parameter.

Thus, for example,

`int-block` is rewritten as

$$\begin{aligned} \text{int-block}(t) = & \\ & \text{exit}(s-env(\xi)); \\ & \text{int-st-list}(s-st-list(t)); \\ & \text{int-decl-part}(s-decl-part(t)); \\ & \text{update-env}(s-decl-part(t)) \end{aligned}$$

and `exit` is now

$$\text{exit}(env) = s-env:env$$

The result of this last change is to obtain one long linear control tree in place of the dump (stack) of control trees.

After the first change to make the machine deterministic, a typical state might appear as in Figure

2.

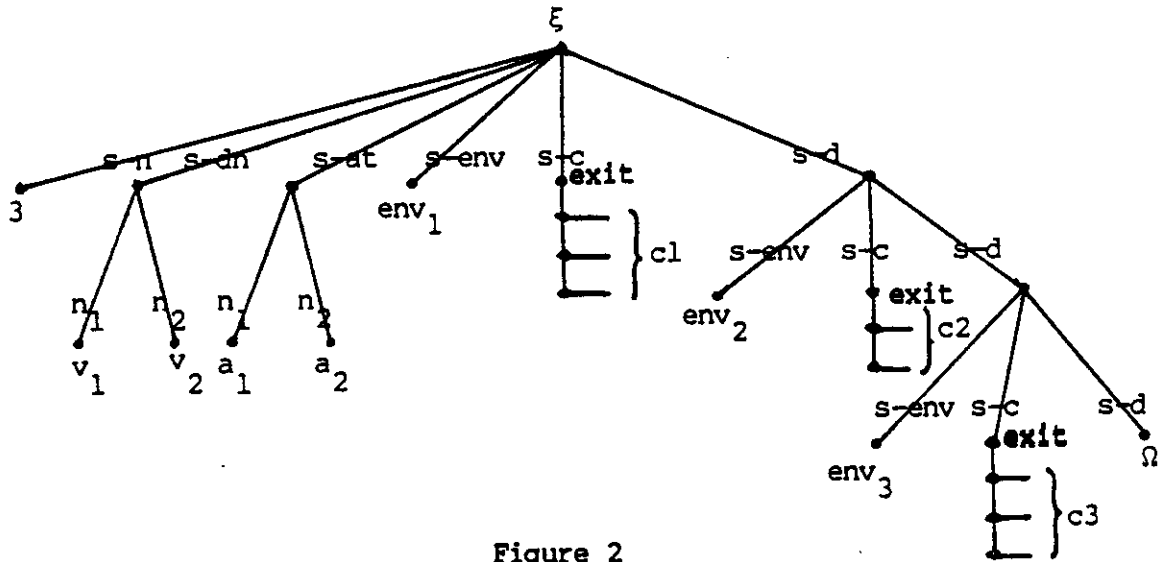


Figure 2

The effect of the second change is to change this state into that of Figure 3.

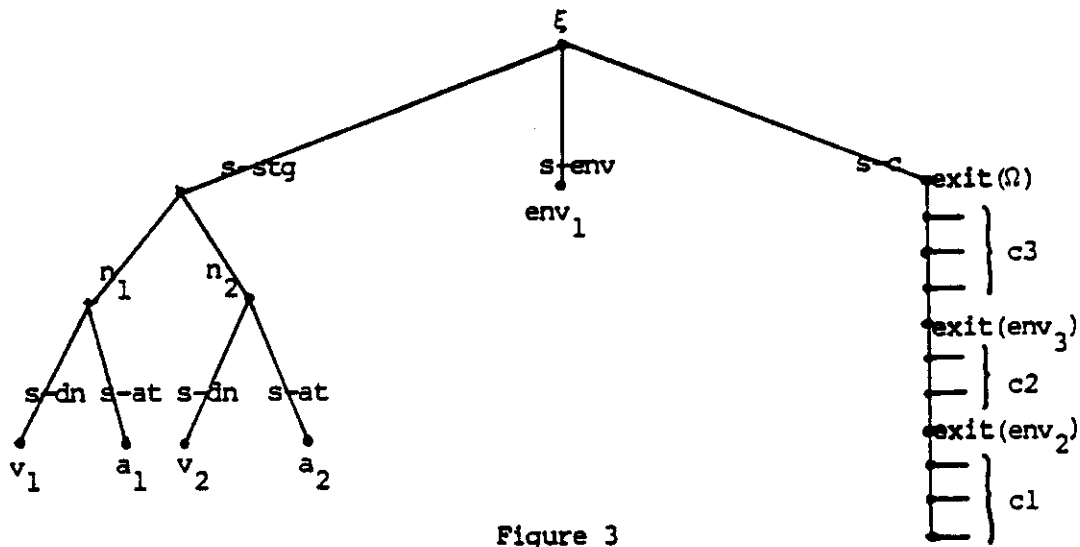


Figure 3

Appendix II contains the definition of EPL+ obtained by applying the above described modification to the definition found in Appendix I.

2.3 Key Observations

Observe what happens to label values by these modifications. In [BW72] it is shown that a label value in the EPL+ machine is a dump,

$$is-label-dn = (\langle s-c:is-c \rangle, \langle s-env:is-env \rangle, \langle s-d:is-d \rangle),$$

the control tree stating what is to be executed from arrival at the labeled statement until the end of the containing block, the environment being that in which these statements are to be executed, and the dump giving the environment, control, and dump to use after exiting this containing block. After these modifications, a label value ends up being simply a control tree paired with an environment,

$$is-label-dn = (\langle s-c:is-c \rangle, \langle s-env:is-env \rangle),$$

the control tree telling how to execute from the labeled statement until the end of the computation and the environment being that in which to execute at least the first instruction in the control tree.

Thus, it is clear that a control tree, after the above modifications, says exactly how to execute from the state owning it until the end of the computation if this end exists and from the state owning it on if the end does not exist. Thus control trees serve the same function as do Mazurkiewicz's tail functions [Maz71]. One can then view the construction of f from Λ as applying the control tree of the current state to the environment and storage of the same state to yield the final state if it exists or \perp if the final state does not exist:

$$\begin{aligned} & \text{if } \xi = \mu_0(\langle s-stg:stg \rangle \langle s-env:env \rangle, \langle s-c:cont \rangle) \text{ then} \\ & f(\xi) = \\ & cont(env, stg) = \begin{cases} \xi_f = \mu_0(\langle s-stg:stg_f \rangle, \langle s-env:env_f \rangle, \langle s-c:cont_f \rangle), & \text{if the computation from } \xi \text{ halts at } \xi_f, \\ \perp, & \text{otherwise.} \end{cases} \end{aligned}$$

Here for the sake of generality f is considered to return a full state, but in the EPL+ machine, $env_f = \Omega$ and $cont_f = \Omega$. Thus, it would suffice to let the result of f be simply stg_f , the final storage, if it exists and \perp otherwise.

A deterministic control tree, such as $cont$, is comprised of one terminal or leaf node and the rest of the tree. In order to execute from ξ until the end, be it a final state or \perp , one must execute the current instruction found in the terminal node, obtaining a new state ξ' and then the rest of the control tree from ξ' until the end. As a consequence, application of $cont$ to env and stg is the execution of $inst$ at $cont$'s terminal node in env and stg to yield a new environment env' , a new storage stg' , and a new control tree $cont'$, followed by application of $cont'$ to env' and stg' . That is,

$$cont(env, stg) = cont'(env', stg'),$$

where $cont'$, env' , and stg' are yielded by executing the instruction $inst$, at $cont$'s terminal node, in env and stg .

2.4 Outline of Construction

The full construction outlined here is found in Sections 2.5 through 2.7. These sections may be skipped if the construction is clear from this outline. Using the above described point of view, one can construct a denotational meaning function which gives the meaning of an instruction in terms of the current environment, the current storage, and what is to be done when the instruction is done. What is to be done when the instruction is done is denoted by the rest of the control tree, *rst*, and the return information, *return-info*, that results from removing the current instruction from the current control tree.

$$\mathcal{M} [\text{Inst}] \text{ env } \langle \text{rst}, \text{return-info} \rangle \text{ stg} = \text{cont}' \text{ env}' \text{ stg}'$$

For each kind of instruction, there is a particular meaning equation scheme for it.

1. For

$$\begin{aligned} \text{Inst} = & \text{PASS:val} \\ & \text{s-stg:stg}' \\ & \text{s-env:env}', \end{aligned}$$

the equation scheme is

$$\mathcal{M} [\text{Inst}] \text{ env } \langle \text{rst}, \text{return-info} \rangle \text{ stg} = \langle \text{rst}, \text{return-info} \rangle \text{ val env}' \text{ stg}'.$$

2. For

$$\begin{aligned} \text{Inst} = & \text{s-stg:stg}' \\ & \text{s-env:env}' \\ & \text{s-c:cont}', \end{aligned}$$

in which *cont'* is the label value for a goto, the equation scheme is

$$\mathcal{M} [\text{Inst}] \text{ env } \langle \text{rst}, \text{return-info} \rangle \text{ stg} = \text{cont}' \Omega \text{ env}' \text{ stg}'.$$

3. For

$$\begin{aligned} \text{Inst} = & \text{rest-of-Inst}(\dots ri \dots); \\ & ri:\text{first-part-of-Inst}, \end{aligned}$$

the equation scheme is

$$\begin{aligned} \mathcal{M} [\text{Inst}] \text{ env } \langle \text{rst}, \text{return-info} \rangle \text{ stg} = \\ \mathcal{M} [\text{first-part-of-Inst}] \text{ env cont}' \text{ stg} \end{aligned}$$

where

$$\begin{aligned} \text{cont}' \text{ val}' \text{ env}' \text{ stg}' = \\ \mathcal{M} [\text{rest-of-Inst}(\dots \text{val}' \dots)] \\ \text{env}' \langle \text{rst}, \text{return-info} \rangle \text{ stg}'. \end{aligned}$$

This informal development has been treating control trees and rests of control trees plus *return-infos* as functions. Actually, they are not, but each can certainly be considered as an encoding of a function which takes information about the state yielded by the execution of an instruction and produces the final result of the ensuing computation. These functions are nothing more than the *continuations* of continuation semantics. It is clear that a continuation requires a value, an environment, and a storage as its arguments. So, define a domain *Cont* with ρ as a typical element as*

$$\rho \in \text{Cont} = \text{is-value} \times \text{is-env} \times \text{is-stg-is-state} + \{\perp\}.$$

However, in the case of EPL as mentioned, the only part of a final state which is not Ω is the storage. Thus, the above could be simplified as

$$\rho \in \text{Cont} = \text{is-value} \times \text{is-env} \times \text{is-stg-is-stg} + \{\perp\}.$$

By denotational semantics convention,

$$\begin{aligned} \text{is-env} &= U, \\ \text{is-dn} &= \text{the domain of storable values} = V, \\ \text{is-stg} &= S, \\ \text{is-stg} + \{\perp\} &= \text{the domain of answers} = A = S + \{\perp\}. \end{aligned}$$

Thus,

$$\rho \in \text{Cont} = V \times U \times S \rightarrow A$$

or in curried form

$$\rho \in \text{Cont} = V \rightarrow U \rightarrow S \rightarrow A.$$

It is now possible to use these continuations in place of the $\langle \text{rst}, \text{return-info} \rangle$ s they represent in the above equation schemes. One obtains a meaning function

$$M \in \text{Inst} \times U \times \text{Cont} \times S \rightarrow A,$$

or in curried form

$$M \in \text{Inst} \rightarrow U \rightarrow \text{Cont} \rightarrow S \rightarrow A.$$

Corresponding to the three equation schemes above one obtains

1. $M \llbracket \text{Inst} \rrbracket \text{ env } \rho \text{ stg} = \rho \text{ val } \text{env}' \text{ stg}'$
2. $M \llbracket \text{Inst} \rrbracket \text{ env } \rho \text{ stg} = \rho' \Omega \text{ env}' \text{ stg}'$
3. $M \llbracket \text{Inst} \rrbracket \text{ env } \rho \text{ stg} = M \llbracket \text{first-part-of-Inst} \rrbracket \text{ env } \rho' \text{ stg}$

where $\rho' \text{ val}' \text{ env}' \text{ stg}' = M \llbracket \text{rest-of-Inst}(\dots \text{val}' \dots) \rrbracket \text{ env}' \rho \text{ stg}'$.

*The Hebrew letter Quf, ρ , is taken as the letter to denote instruction continuations here so as not to confuse it with the more specific command and expression continuations which have their own naming conventions.

In (2) above, p' is the label value of a goto.

Appendix IID contains the denotational continuation semantic definition of EPL+ constructed from the VDL definition of EPL+ of Appendix II.

The previous paragraphs have effectively shown how to systematically construct a denotational continuation semantic definition from a VDL semantic definition of the form described in Section 2.2. The constructed definition is such that

1. the parameters of the statement continuation are precisely the top-level state components other than the control tree, and
2. there is one equation for each instruction (action) of the three instruction forms mentioned above.

2.5 Value-Returning Instructions - Passing Value Back

There are two possibilities as to the nature of *inst* as it is executed. It may be either value-returning or macro. In the case of a value-returning instruction, one possibility is that it is of the form (as for expression evaluation with side effects):

```
inst =
    PASS:val
    s-stg:stg'
    s-env:env'
```

Then *env'* and *stg'* are the result of executing *inst* in *env* and *stg* and are in general different from *env* and *stg*. Additionally, *cont'* is related to *cont* in the following way: Let *rst* be the rest of *cont*, i.e., *cont* after the terminal node is removed. Then *cont'* is *rst* as modified by the passed-up value, *val*, returned by the instruction. Recall that part of a node is information stating to which actual parameters of which instructions higher up the tree the PASSEd value should be copied.

Thus, if *cont* is considered as composed of (*return-info,inst*);*rst*, i.e., *cont* appears as in figure A.



and execution of *inst* in *env* and *stg* yields *env'*, *stg'*, and *val* as the new environment, new storage, and PASSEd value respectively. Then,

$$\begin{aligned}
 cont(env, stg) &= \text{execute } inst \text{ in } env \text{ and} \\
 &\quad stg \text{ followed by doing} \\
 &\quad rst \text{ with the help of } return-info \\
 &= rst-as-modified-by-val-according- \\
 &\quad to-return-info(env', stg') \\
 &= cont'(env', stg').
 \end{aligned}
 \tag{e1}$$

All elements of the above equation yield either the same final state or \perp as the case may be.

The middle two elements of the above equation e1 may be used to define the meaning of an instruction *Inst* as a function of the current environment, *env*, the current storage, *stg*, and what is to be done after the instruction, *rst-as-assisted-by-return-info*. That is,

$$\begin{aligned} &\text{meaning of } Inst \text{ in } env, stg, rst, \\ &\text{and } return-info = \\ &rst-as-modified-by-val-according-to- \\ &return-info(env', stg') \end{aligned} \tag{e2}$$

Examination of the parts of equation e2 shows that only *rst* and *return-info* appear on both sides. Thinking about these two items as a unit shows that it is *rst* and *return-info* combined that tell how to continue from the completion of *Inst*. That is this pair, given the value, environment and storage returned by *Inst*, tells how continue. Taking *rst* and *return-info* as a unit, e2 can be rewritten as:

$$\begin{aligned} &\text{meaning of } Inst \text{ in } env, stg \text{ and} \\ &\langle rst, return-info \rangle = \\ &\langle rst, return-info \rangle(val)(env', stg'), \end{aligned} \tag{e3}$$

where execution of *Inst* in *env* and *stg* yields *val*, *env'*, and *stg'*. Both sides of this equation yield the same final state or \perp as the case may be.

This informal development has been treating control trees and rests of control trees plus *return-infos* as functions. Actually, they are not, but each can certainly be considered as an encoding of a function which takes information about the state yielded by the execution of an instruction and produces the final result of the ensuing computation. These functions are nothing more than the *continuations* of continuation semantics. As e3 is in terms of $\langle rest-of-control-tree, return-info \rangle$ pairs, the continuations encoded by them are considered first. From e3, it is clear that a continuation requires a value, an environment and a storage as its arguments. So, define a domain *Cont* with ρ as a typical element as

$$\rho \in Cont = is-value \times is-env \times is-stg-is-state + \{\perp\}.$$

However, in the case of EPL as mentioned, the only part of a final state which is not Ω is the storage. Thus, the above could be simplified as

$$\rho \in Cont = is-value \times is-env \times is-stg-is-stg + \{\perp\}.$$

By denotational semantics convention,

$$\begin{aligned} is-env &= U, \\ is-dn &= \text{the domain of storable values} = V, \\ is-stg &= S, \\ is-stg + \{\perp\} &= \text{the domain of answers} = A = S + \{\perp\}. \end{aligned}$$

Thus,

$$\rho \in Cont = V \times U \times S \rightarrow A$$

or in curried form

$$\rho \in Cont = V \rightarrow U \rightarrow S \rightarrow A.$$

It is now possible to formalize the notion of a meaning function on instructions, environments, storages and continuations (representable by $\langle rest-of-control-tree, return-info \rangle$ pairs) to answers. Historically, the order of the parameters of the meaning function is instructions, environments, continuations, and storages because it gives a more useful currying. Letting *Inst* be the domain of control tree instructions,

$$Mean \in Inst \times U \times Cont \times S \rightarrow A,$$

or in curried form,

$$Mean \in Inst \rightarrow U \rightarrow Cont \rightarrow S \rightarrow A.$$

Following e3, the equation defining the meaning of an instruction is:

$$Mean[[Inst]] env \wp stg = ans = \wp val env' stg' \tag{e3a}$$

where

$$\begin{aligned} Inst &\in Inst, \\ env, env' &\in U, \\ \wp &\in Cont, \\ stg, stg' &\in S, \\ ans &\in A, \text{ and} \\ val &\in V, \end{aligned}$$

and where execution of *Inst* in *env* and *stg* yields *val*, *env'* and *stg'*. The equation may be read, "The meaning of *Inst* in *env* and *stg* in the presence of the continuation \wp is simply the answer obtained by applying the continuation to the *val*, *env'*, and *stg'* yielded by the instruction in *env* and *stg*."

If the instruction of a particular group of instructions corresponding to some syntactic domain always leave the environment or storage unchanged or do not pass up a value, a special continuation for that syntactic domain can be designed in which the unchanged component or the returned value is left out as a parameter of the continuation. For example, in many languages, commands (statements) do not modify environments and return no value, so one might use a command continuation $\theta \in CCont$, which is on storage only:

$$\theta \in CCont = S \rightarrow A.$$

Also in many languages, expressions have no side effects on either the environment or storage. Thus, it is reasonable to designate an expression continuation, $\kappa \in ECont$, as being on values only:

$$\kappa \in ECont = V \rightarrow A.$$

Thus, the command continuation needs only the new storage that a command generates to go to the final answer; it assumes that the environment is unchanged and no value is passed up by the command. Likewise, the expression continuation needs only the returned value to go to the final answer; it assumes that the environment and storage are unmodified by the expression.

It is now possible to identify the function encoded by *cont* and *cont'* in the expressions

$$cont(env, stg)$$

and

$$cont'(env', stg').$$

The function they encode is a statement continuation, that is a continuation for a construct which modifies the environment and storage but does not return a value. Such a continuation has the functionality

$$U \rightarrow S \rightarrow A.$$

Define the domain $SCont$ with typical element \beth :*¹

$$\beth \in SCont = U \rightarrow S \rightarrow A.$$

Note that $cont'$ is produced by modifying rst by val according to $return-info$. That is, applying a general continuation $\wp \in Cont$ to a value yields a statement continuation, \beth . This statement continuation represents the execution after completion of the statement containing the expression returning the value. So to speak, the general continuation swallows the value returned by the expression to make the continuation for the containing statement.

1.6 Value-Returning Instructions - Assigning Control Tree

Consider again the application of a control tree $cont$ to env and stg . Suppose that the instruction $inst$ of the terminal node of $cont$ is a value-returning instruction of the form (e.g., as for a goto or a backtracking):

$$\begin{aligned} inst = & \\ & s-stg:stg' \\ & s-env:env' \\ & s-c:cont' \end{aligned}$$

The rules of VDL are such that if the control tree is replaced in a value-returning instruction then no node containing it can have $return-info$.** In this case env' , stg' and $cont'$ are the result of executing $inst$ in env and stg and are in general different from env , stg , and $cont$. In this case,

$$\begin{aligned} cont(env, stg) &= \text{execute } inst \text{ in } env \text{ and} \\ &\quad stg \text{ followed by doing} \\ &\quad rst \text{ with the help of} \\ &\quad return-info \\ &= \text{execute } inst \text{ in } env \text{ and} & (e4) \\ &\quad stg \text{ thus ignoring} \\ &\quad rst \text{ and } return-info \\ &\quad \text{entirely} \\ &= cont'(env', stg'). \end{aligned}$$

That is, execution of the instruction, $inst$, causes the rest of the control tree, $cont$, on which it was found to be ignored entirely and for the $cont'$ to be used to dictate the rest of the computation based on the env' and stg' returned by the instruction.

The meaning of such an instruction can be exhibited by an equation such as

$$\begin{aligned} \text{meaning of } inst \text{ in } env, stg, \text{ and} \\ \langle rst, return-info \rangle &= & (e5) \\ cont'(env', stg'). \end{aligned}$$

This may be formalized using the functions introduced earlier. Suppose $\langle rst, return-info \rangle$ encodes \wp and $cont'$ encodes \beth . Then,

* \beth is the Hebrew letter Kaf.

**A replaced control tree combined with the presence of $return-info$ gives rise to a μ with two dependent composite selectors.

$$\text{Mean}[\text{Inst}]env \wp \text{stg} = \wp \text{env}' \text{stg}' \quad (\text{e6})$$

where execution of *Inst* in *env* and *stg* yields \wp , *env'*, and *stg'*.

1.7 Macro Instruction

Recall that $\text{cont} = (\text{return-info}, \text{Inst}); \text{rst}$, i.e., as shown in figure A. Consider now the application of *cont* to *env* and *stg* when the instruction, *Inst*, of the terminal node of *cont* is a macro instruction of the form:

Inst =
rest-of-Inst(. . . ri . . .);
ri: first-part-of-Inst

where *ri* may be empty. That is, the application of *cont* to *env* and *stg* is the execution of *Inst* which yields the same environment, *env*, the same storage, *stg*, and the control tree, $\text{cont}' = (\text{ri}, \text{first-part-of-Inst}); (\text{return-info}, \text{rest-of-Inst}(. . . \text{ri} . . .)); \text{rst}$, as shown in figure B,

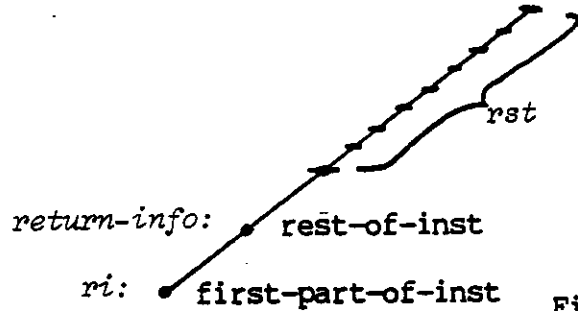


Figure B

followed by application of *cont'* to *env* and *stg*. Equationally,

$$\begin{aligned} \text{cont}(env, stg) &= \text{execute Inst in env and} \\ &\quad \text{stg followed by doing} \\ &\quad \text{rst with the help of} \\ &\quad \text{return-info} \quad (\text{e7}) \\ &= \text{cont}'(env', stg'). \end{aligned}$$

However, application of *cont'* to *env* and *stg* is the execution of *first-part-of-Inst* in *env* and *stg* followed by doing *rest-of-Inst*(. . . *ri* . . .) with the help of *ri* followed by doing *rst* with the help of *return-info*. Thus,

$$\begin{aligned} &\text{execute Inst in env and stg followed by doing} \\ &\quad \text{rst with the help of return-info} \\ &= \text{cont}'(env, stg) \\ &= \text{execute first-part-of-Inst in env and stg} \quad (\text{e8}) \\ &\quad \text{followed by doing rest-of-Inst}(. . . \text{ri} . . .) \text{ with} \\ &\quad \text{the help of ri followed by doing} \\ &\quad \text{rst with the help of return-info.} \end{aligned}$$

In other words, when execution of *Inst* requires execution of *first-part-of-Inst* followed by doing *rest-of-Inst*(. . . *ri* . . .) with the help of *ri*, then execution of *Inst* followed by doing *rst* with the help of *return-info* is execution of *first-part-of-Inst* followed by doing *rest-of-Inst*(. . . *ri* . . .) with the help of *ri* followed by doing *rst* with the help of *return-info*. In such a case, the meaning of *Inst* is defined in terms of the meaning of *first-part-of-Inst*.

$$\begin{aligned}
&\text{meaning of } \text{inst} \text{ in } \text{env}, \text{stg}, \text{ and} \\
&\langle \text{rst}, \text{return-info} \rangle = \\
&\text{meaning of } \text{first-part-of-inst} \text{ in } \text{env}, \text{stg}, \\
&\text{and } \langle \text{rest-of-inst}(\dots ri \dots); \langle \text{rst}, \text{return-info} \rangle, ri \rangle
\end{aligned} \tag{e9}$$

Letting $\langle \text{rst}, \text{return-info} \rangle$ be an encoding of ρ , e9 can be formalized as

$$\begin{aligned}
&\text{Mean}[\text{inst}] \text{env } \rho \text{ stg} = \\
&\text{Mean}[\text{first-part-of-inst}] \text{env } \rho' \text{ stg}
\end{aligned} \tag{e10}$$

where

$$\begin{aligned}
&\rho'(val', env', stg') = \\
&\text{Let } \text{cons}'' = \text{rest-of-inst}(\dots ri \dots) \text{-modified-by-} \\
&\quad \text{val}' \text{-according-to-ri} \text{ in} \\
&\text{apply } \text{cons}'' \text{ to } \text{env}' \text{ and } \text{stg}' \text{ to yield} \\
&\text{val}'', \text{env}'', \text{ and } \text{stg}'' \text{ which are} \\
&\text{passed as parameters to } \rho
\end{aligned}$$

That is, ρ' ,

1. first, given the val' , env' , and stg' returned by $\text{first-part-of-inst}$, does the effect of $\text{rest-of-inst}(\dots ri \dots)$ to obtain val'' , env'' , and stg'' ,
2. second, passes val'' , env'' and stg'' to ρ to obtain the final answer.

Note that

$$\text{rest-of-inst}(\dots ri \dots) \text{-modified-by-} \text{val}' \text{-according-to-ri}$$

is nothing more than $\text{rest-of-inst}(\dots val' \dots)$.

The meaning equation, e10, for inst reflects inst 's macro nature in that the macro arranges for the execution of an expanded list of instructions, starting with $\text{first-part-of-inst}$, to achieve its effect rather than directly modifying the environment and storage by itself.

3. Formalization of Construction of Continuation from Deterministic Control Tree

All of the development of Section 2 may be formalized using the concepts developed in the formal definition of the VDL programming language as given in [LLS70] and in [LW69]. Using the notation of the former, this Section shows the construction from a deterministic control tree to the continuation it is an encoding of.

First it is necessary to outline the formal definition of the VDL programming language, adapting it to fit the restricted states and control trees used in the informal development of the previous Section.

3.1 Control Trees in General

First recall that for any state ξ ,

$$\text{is-c}(s\text{-c}(\xi))$$

and that

$$is-c = is-ct \vee is-\Omega.$$

That is, the $s-c$ component of any state either is a control tree or is Ω . A state ξ such that $s-c(\xi) = \Omega$ is a final state.

A control tree in general is described by

$$is-ct = (\langle s-in:is-in \rangle, \langle s-el: (\{ \langle elem(i):is-ob \rangle \parallel is-intg(i) \}) \rangle, \langle s-ri: (\langle s-comp:is-sel \rangle, \langle s-ap:is-ip-set \rangle) \rangle, \{ \langle r:is-ct \rangle \parallel r \in R \})$$

where: $is-intg = \{1, 2, \dots\}$
 $is-in$ = set of instruction names
 $is-sel$ = set of all selectors, i.e., $is-sel = S^n$
 $is-ob$ = set of all objects
 $is-ip = (\langle elem(1):is-intg \rangle, \langle elem(2):is-intg \rangle)$
 R is an infinite set of simple selectors such that $R \cap \{s-in, s-el, s-ri, s-comp, s-ap\} = \emptyset$

Note that the $s-el$ (argument list) component of a control tree may not be a proper list, because some of the indices less than the maximum may select Ω .

For convenience, the predicate $is-ri$ is introduced to denote the set of *return-Infos*

$$is-ri = (\langle s-comp:is-sel \rangle, \langle s-ap:is-ip-set \rangle)$$

A few useful functions are nd , yielding for a control tree the set of composite selectors selecting the nodes of the tree,

$$nd(ct) = \{ \chi \mid \chi \in R^n \ \& \ \chi(ct) \neq \Omega \},$$

$pred$, yielding for a composite selector selecting a node, the composite selector selecting one node higher up the tree,

$$pred(\chi) = (\iota \chi_1) ((\exists s \in S) (s \circ \chi_1 = \chi)),$$

and $pred^n$, the obvious extension of $pred$:

$$pred^n(\chi) = \begin{matrix} n=0 \rightarrow \chi \\ \top \rightarrow pred^{n-1}(pred(\chi)) \end{matrix}$$

3.2 Deterministic Control Trees

Now as a result of the restrictions to the deterministic case, it is known that if $is-ct(ct)$ then for at most one selector $r \in R$, $r(ct) \neq \Omega$.

3.2.1 Control Tree Representations

What appears in the VDL programming language wherever a control tree or sub-control tree is needed is a two-dimensional (i.e. indentation as well as the text itself counts) representation of the control tree object as defined above. Because of the determinism assumption, a control tree representation, *ct-rep*, has one of the following forms:

1. *instr*
2. *instr*;
 succ.

An *instr* has one of the forms

1. *ln*
2. *ln*(*expr*₁, . . . , *expr*_{*n*}),

and *succ* has one of the forms

1. *ct-rep*
2. *dum:ct-rep*
3. *expr*(*dum*):*ct-rep*.

To obtain from a control-tree representation the control tree it represents; first the dummy names, *dum*, are eliminated by the following two rules:

1. Each occurrence of a dummy name, *dum*, as the prefix of an instruction (as in the 2nd and 3rd forms of a *succ*) is replaced by a set of integer pairs: $\langle i, j \rangle$ is in this set if and only if the same *dum* appears as the *j*th actual parameter of the *i*th predecessor node.
2. All uses of dummy names in actual parameter positions are replaced by Ω .

Then the control tree represented by a given control tree representation is determined recursively by Table 1 [LLS70] given below.

3.2.2 Instruction Schemata

The instruction schemata (i.e., instruction definition in the VDL programming language) associated with an instruction name *ln* has, in general, the following form:

$$\begin{aligned} \text{ln}(x_1, \dots, x_n) = & \\ & p_1(x_1, \dots, x_n, \xi) \rightarrow \text{group}_1(x_1, \dots, x_n, \xi) \\ & \vdots \\ & \vdots \\ & p_m(x_1, \dots, x_n, \xi) \rightarrow \text{group}_m(x_1, \dots, x_n, \xi) \end{aligned}$$

where each p_i is a meta expression denoting a truth value and each group_i has one of the two forms:

Category	Form	Represented Control Tree
<i>ct-rep</i>	<i>instr</i>	<i>instr</i>
	<i>instr</i> ; <i>succ</i>	$\mu(\text{instr}; \{r:\text{succ}\})$ where $r \in R$
<i>instr</i>	<i>in</i>	$\mu_o(\langle s\text{-in:in} \rangle)$
	<i>in</i> (<i>expr</i> ₁ , ..., <i>expr</i> _{<i>n</i>})	$\mu_o(\langle s\text{-in:in} \rangle, \langle s\text{-al:} \mu_o(\langle \text{elem}(1):\text{expr}_1 \rangle, \dots, \langle \text{elem}(n):\text{expr}_n \rangle) \rangle)$
<i>succ</i>	<i>ct-rep</i>	$\mu(\text{ct-rep}; \langle s\text{-ri:} \mu_o(\langle s\text{-comp:l} \rangle, \langle s\text{-ap:\{ \} \rangle) \rangle)$
	<i>dum*</i> : <i>ct-rep</i>	$\mu(\text{ct-rep}; \langle s\text{-ri:} \mu_o(\langle s\text{-comp:l} \rangle, \langle s\text{-ap:dum*} \rangle) \rangle)$
	<i>expr</i> (<i>dum*</i>): <i>ct-rep</i>	$\mu(\text{ct-rep}; \langle s\text{-ri:} \mu_o(\langle s\text{-comp:expr} \rangle, \langle s\text{-ap:dum*} \rangle) \rangle)$

where: *dum** is the set of ordered pairs which has replaced *dum*.

Table 1

1. value returning:

PASS: $\epsilon_v(x_1, \dots, x_n, \xi)$
s-env: $\epsilon_\rho(x_1, \dots, x_n, \xi)$
s-stg: $\epsilon_\sigma(x_1, \dots, x_n, \xi)$
s-c: $\epsilon_\rho(x_1, \dots, x_n, \xi)$

where each ϵ_i is an arbitrary expression; any of the lines may be missing; a missing first (PASS) line is taken as equivalent to PASS: Ω .

2. macro:

ct-rep(x_1, \dots, x_n, ξ)

a control tree representation.

It is now possible to define a function *cont* which extracts the continuation encoded by a control tree and return-information pair.

Each instruction schema can be considered a shorthand for a function,

$\Delta_{\text{is-ob}}^n \times \text{is-state} \times \text{is-sel} \times \text{is-ri-is-state} \times \text{is-ob}$,

$\Delta_{\text{is-ob}}(x_1, \dots, x_n, \xi, \tau, ri) =$
 $p_1(x_1, \dots, x_n, \xi) \rightarrow \text{group}_1^+(x_1, \dots, x_n, \xi, \tau, ri)$
 \vdots
 \vdots

$$p_m(x_1, \dots, x_n, \xi) \rightarrow group_m^+(x_1, \dots, x_n, \xi, \tau, ri)$$

where $group_i^+$ is obtained from $group_i$ according to the latter's form: If $group_i$ is

1. value returning then $group_i^+$ is

$$\begin{aligned} & \langle \mu(\xi; \{ \langle s\text{-comp}(ri) \circ elem(j) \circ s\text{-al}(\text{pred}^i(\tau)) \circ s\text{-c}: \\ & \quad \epsilon_v(x_1, \dots, x_n, \xi) \rangle \mid \langle i, j \rangle \in s\text{-sp}(ri) \}); \\ & \quad \langle s\text{-env}: \epsilon_\rho(x_1, \dots, x_n, \xi) \rangle, \\ & \quad \langle s\text{-stg}: \epsilon_\sigma(x_1, \dots, x_n, \xi) \rangle, \\ & \quad \langle s\text{-c}: \epsilon_\rho(x_1, \dots, x_n, \xi) \rangle, \\ & \quad \epsilon_v(x_1, \dots, x_n, \xi) \rangle \end{aligned}$$

2. macro, then $group_i^+$ is

$$\begin{aligned} & \langle \mu(\xi; \langle \tau \circ s\text{-c}: \mu(ct\text{-rep}(x_1, \dots, x_n, \xi); \langle s\text{-ri}: ri \rangle) \rangle), \\ & \quad \Omega \rangle \end{aligned}$$

That is Δ_m returns both the next state, à la Φ_m defined in [LW69], and the passed up value.

Using this Δ_m , it is possible to write a recursive function, $cont$, which given a deterministic control tree ct (with therefore a unique terminal node) and return information ri , stating where in ct a returned value is to be passed, constructs the continuation encoded by them. That is:

$$cont: is\text{-}ct \times is\text{-}ri \rightarrow Cont$$

The continuation yielded by $cont(ct, ri)$ is of type

$$V \times E \times S \rightarrow A.$$

Figure 4 is useful for understanding the definition of $cont$.

Definition 1. Suppose $is\text{-}c(ct)$ and $is\text{-}ri(ri)$.

Then,

$$\begin{aligned} cont(ct, ri) &= \lambda(\epsilon, \rho, \sigma). \\ & \quad (is\text{-}\Omega(ct) \rightarrow \sigma, \\ & \quad T\text{-}cont(ct', ri)(\epsilon', \rho', \sigma')) \end{aligned}$$

where

$$\begin{aligned} tn(ct) &= (\iota\tau)(\tau \in \text{nd}(ct) \ \& \ (\forall \chi)(\chi \in \text{nd}(ct) \supset \text{pred}(\chi) \neq \tau)), \\ \tau &= tn(ct), \\ ct' &= \mu(ct; \{ \langle s\text{-comp}(ri) \circ elem(j) \circ s\text{-al}(\text{pred}^{i-1}(\tau)): \epsilon \rangle \mid \langle i, j \rangle \in s\text{-sp}(ri) \}), \end{aligned}$$

$$\begin{aligned}
node &= \tau(ct^\dagger), \\
in &= s-in(node), \\
al &= s-al(node), \\
nri &= s-ri(node), \\
n &= \text{number of formal parameters in schema for } s-in(node), \text{ and} \\
\langle \mu_o(\langle s-env:\rho' \rangle, \langle s-stg:\sigma' \rangle, \langle s-c:ct' \rangle), \epsilon' \rangle &= \\
&\Delta_{tn}(elem(1,al), \dots, elem(n,al), \\
&\mu_o(\langle s-env:\rho \rangle, \langle s-stg:\sigma \rangle, \langle s-c:\delta(ct^\dagger, \tau) \rangle), \tau, nri)
\end{aligned}$$

In the above, tn identifies the selector τ selecting the terminal node of ct . Also, recall that $\delta(O, \chi)$ deletes the object at $\chi(O)$ from O .

4. From Nondeterministic Control Trees to Parallel Continuations (Informal Discussion)

The ultimate goal of this Section is to obtain a parallel continuation which can be used in equations to define the meanings of syntactic constructs in a denotational, syntax-directed manner. Section 3 started with deterministic control trees and obtained by a particular construction continuations and equations such that the control trees and continuations could be used interchangeably in the equations. Thus, this Section starts with nondeterministic control trees and follows the same construction in an attempt to obtain parallel continuations and equations such that the control trees and the continuations can be used interchangeably in these equations. That is, this Section takes the paradigm of applying the nondeterministic control tree to the rest of the state. The results are a parallel continuation and a rather messy equation. In the hopes that the control trees and the continuations can be used in the equations, the messiness of the equations is tolerated. It turns out that only the control trees may be used in the equations. Examination of the equations shows why continuations cannot be used in them and suggests a cleaner formulation of the equations suitable for use with the control trees only. It does however turn out that from any control tree used in either of the equation forms, one can obtain the parallel continuation that it encodes.

4.1 Unmodifications and New Modifications to EPL+ Machine

First undo the changes to the EPL+ machine of Section 2.2 which made the control tree strictly deterministic. That is, the commas are re-introduced and the macros which had originally expanded into sets of instructions are put back into that form. At this point, a typical state looks as shown in Figure 5. The control tree may have several terminal nodes. At any given state, any terminal node may be selected for execution -- giving rise to nondeterminism in the computation sequence.

If EPL+ were now extended to permit parallel blocks, e.g.,

`par $s_1; \dots; s_n$ end,`

a sequence of statements which are executed asynchronously (i.e., possibly completely interleaved parallelism) some additional changes to the state and the instruction definitions must be made. Since the execution of each of these statements may independently enter blocks, procedures, and functions and may do gotos, each must be able to operate in its own environment rather than from a single global shared environment. They must, however, all share the same global storage, otherwise the nondeterminism will not make itself felt through possible nondeterminate results in a shared storage.

Thus, the environment must be removed from the top level of the state and made a parameter of each instruction which itself can access identifiers or which expands to at least one instruction which can access identifiers. As a result, a typical state ends up appearing as in Figure 6. Observe that it is no longer necessary for `exit` to have an environment as a parameter since there is no global environment to restore. Observe also that a label value is simply a control tree, as its environment is found directly in the instructions to execute after the `goto`.

$\rho \in \text{is-env} = U$
 $\rho' \in \text{is-env} = U$
 $\sigma \in \text{is-stg} = S$
 $\sigma' \in \text{is-stg} = S$
 $\epsilon \in \text{is-den} = V$
 $\epsilon' \in \text{is-den} = V$

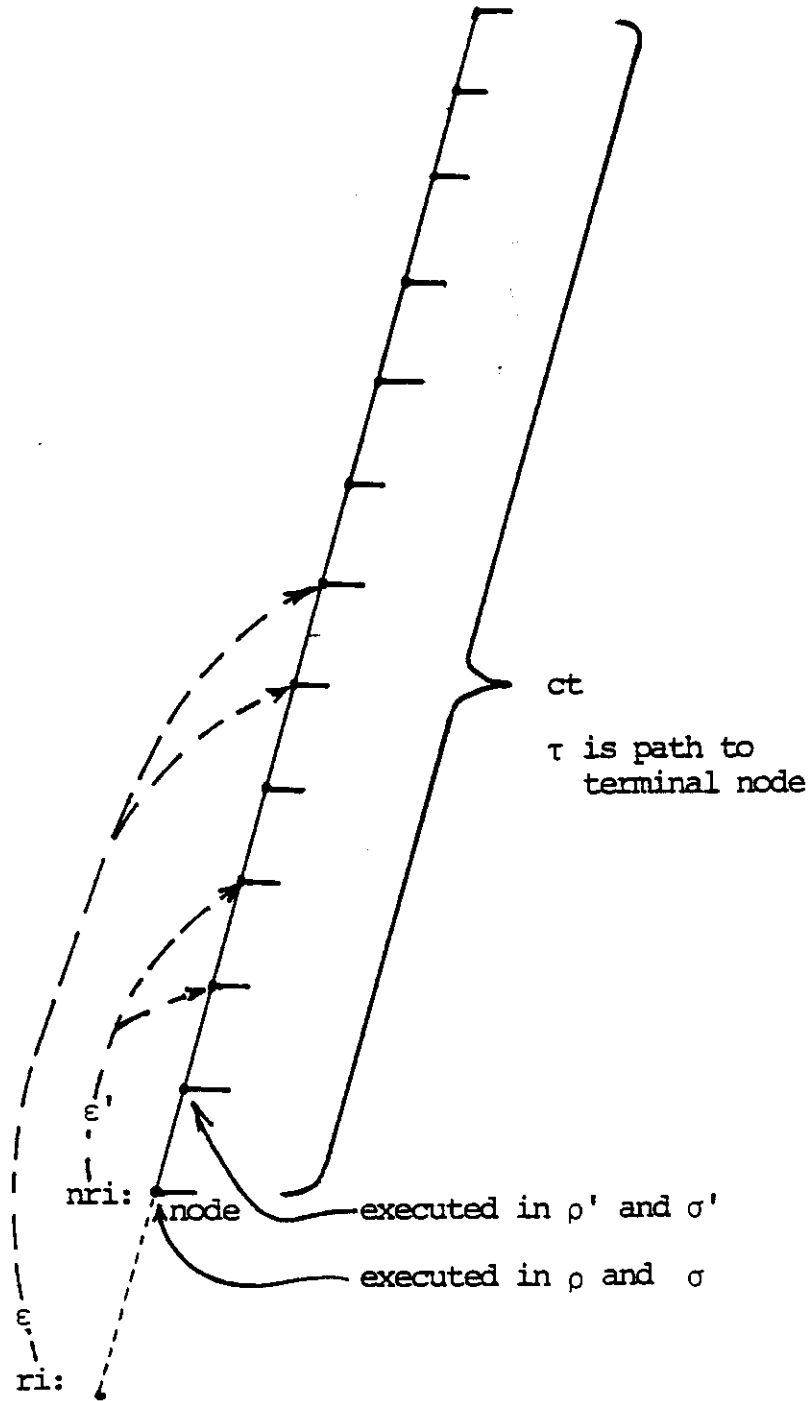


Figure 4

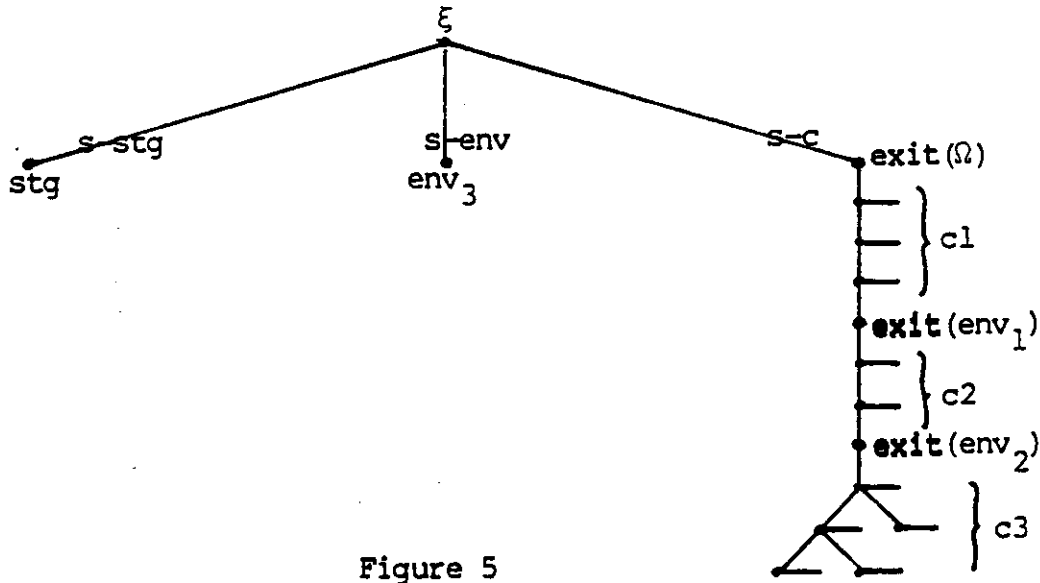


Figure 5

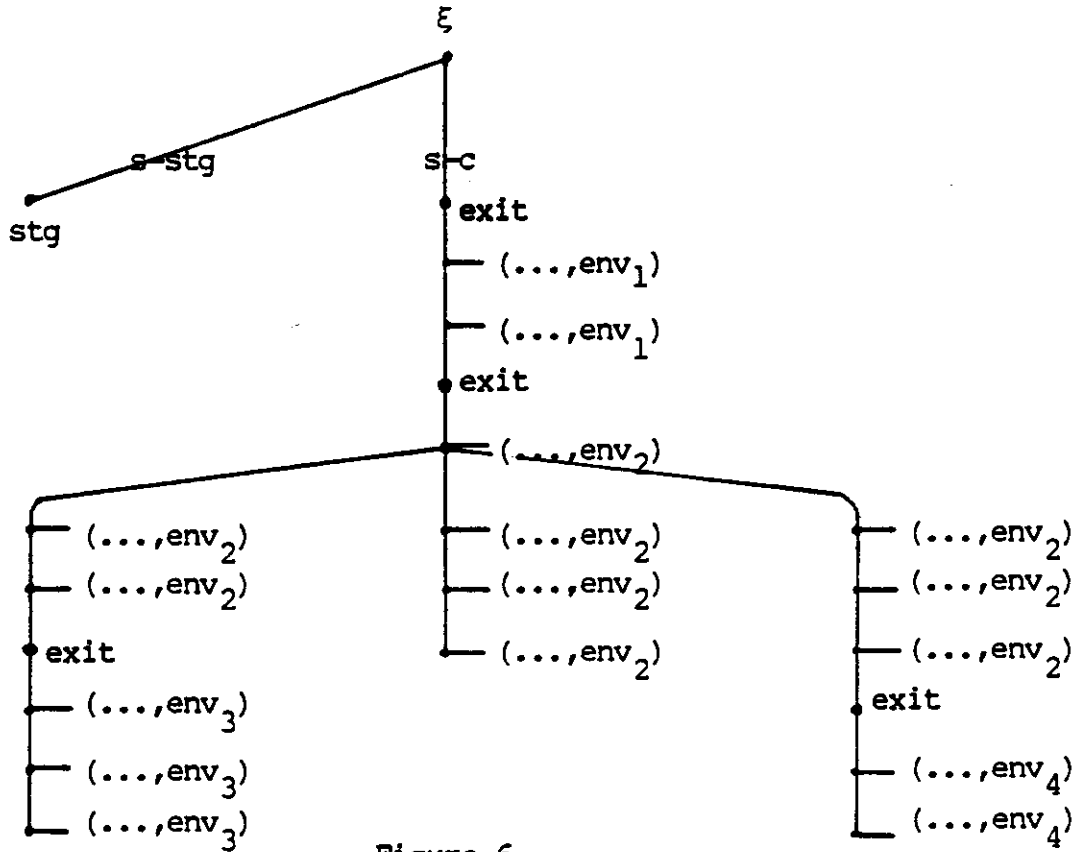


Figure 6

Appendix III contains a definition of EPL+ extended with a parallel block.

4.2 Nondeterministic Control Trees

A nondeterministic control tree, *cont*, has a set of terminal nodes. At any step in a computation, some one of these nodes is selected. Once a node is selected, the rest of the tree, *rst*, is determined, i.e., the original tree with the selected node removed. In order to know how to pass values up or to where to attach macro subtrees it is necessary to remember along with *rst* the composite selector χ selecting the removed node as well as the return information. This is illustrated in Figure 7.

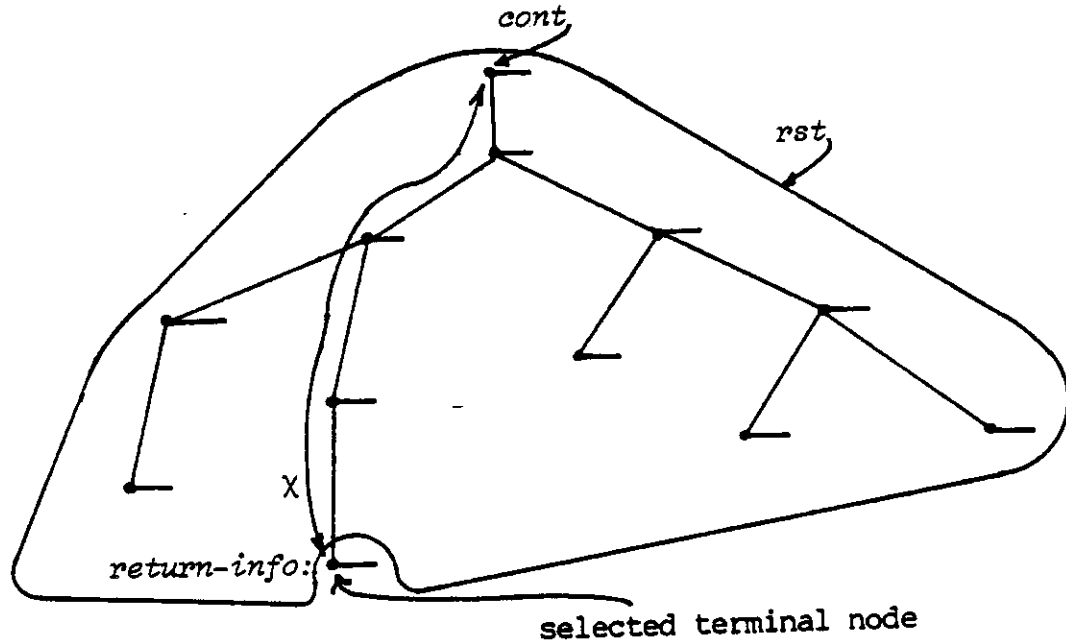


Figure 7

Just as in the deterministic case *rst* and *return-info* were identified as a unit, so are *rst*, χ , and *return-info* identified as a unit in the nondeterministic case. It is convenient to call the *rst*, χ and *return-info* associated with a node η of *cont* the *p-rest* of *cont* with respect to η and to call triples of *rst*s, χ s, and *return-infos* *p-rests*^{*}. If one observes that in the deterministic case, the χ of a terminal node is uniquely determined then it is clear that, in fact, the $\langle \textit{rst}, \textit{return-info} \rangle$ pair for a deterministic control tree conveys the same information as the $\langle \textit{rst}, \chi, \textit{return-info} \rangle$ triple for a nondeterministic control tree.

4.3 Computations and Λ

To execute from a state ξ with control tree *cont* until the end, starting with this particular selection of terminal node, it is necessary to execute the instruction at the selected terminal node. When it or its expansion is completed then *rst*, possibly modified by a returned value according to *return-info* is executed until the end.

^{*}"p" for parallel.

The *rst* itself in general has a set of terminal nodes, some of which may be terminal nodes in *cont* which were not selected for execution. As a consequence, for each terminal node of *cont*, there is a different beginning of the remainder of the computation, and for each such remainder, for each terminal node of the possibly modified *rst* there is a different beginning of the remainder of the remainder of the computation. Thus, from a given control tree *cont*, there is a tree of possible computations, as illustrated in Figure 8.

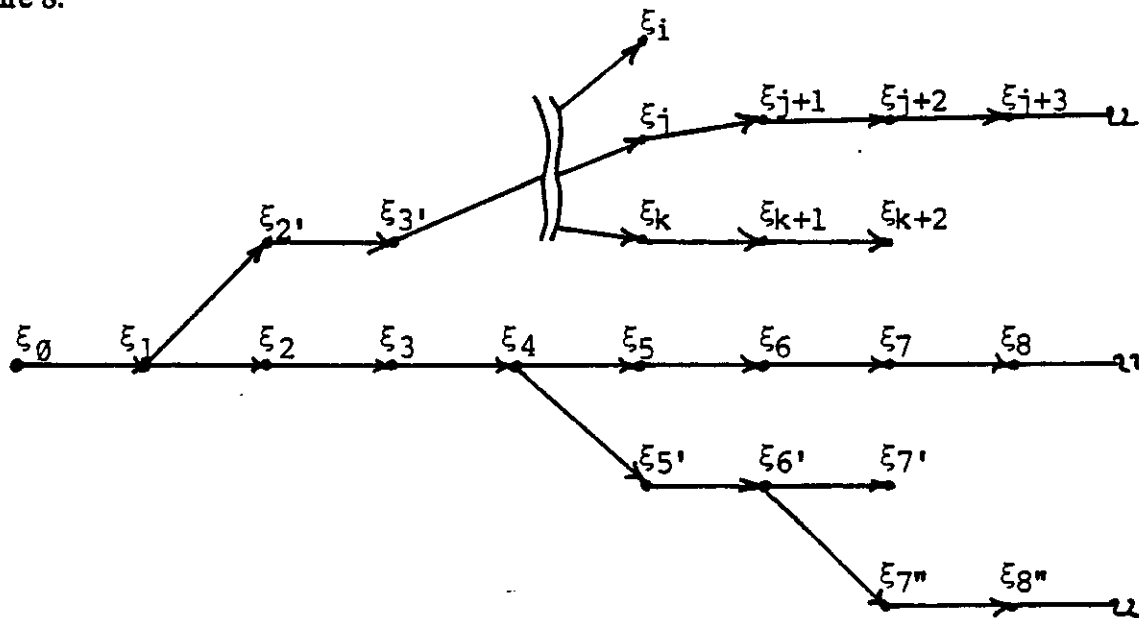


Figure 8

Each node of the tree is a state and each path, possibly infinite, is the computation comprised, in sequence, of the states lying on it.

The usual way to fit nondeterministic computations into the information structure model framework is to let the Λ of the triple,

$$(is\text{-}state, is\text{-}initial\text{-}state, \Lambda),$$

return for a state a set of states.

$$\Lambda: is\text{-}state \rightarrow \mathcal{P}(is\text{-}state).$$

A transition from state ξ is accomplished by selecting any element of $\Lambda(\xi)$. If $\Lambda(\xi)$ returns the empty set, the computation is said to halt at ξ .

By analogy to the discussion of Section 2, the construction of f from Λ may be viewed as giving for a particular initial state the set of answers obtainable from all computations of that initial state, i.e., the set of all final states and \perp if any computation is nonterminating:

$$f: is\text{-}state \rightarrow \mathcal{P}(is\text{-}state + \{\perp\})$$

From this discussion it is clear that application of a control tree to the remainder of the state, i.e., the storage, should be viewed as yielding a set of answers and that a nondeterministic control tree is an encoding either of a set of classical continuations each yielding an answer or of a *parallel continuation* yielding a set of answers.

4.4 Application of Nondeterministic Control Trees

Let $cont$ be a nondeterministic control tree and let stg be storage. Then application of $cont$ to stg gives rise to a set of answers. A subset of these answers is obtained by selecting one of the terminal nodes of $cont$, say η , executing its instruction $inst$ in stg and its p -rest to yield new storage stg' and a new control tree $cont'$, and then applying $cont'$ to stg' . The application of $cont'$ to stg' itself yields the required set of answers.

To be more precise, suppose $\{\eta_1, \dots, \eta_n\}$ is the set of terminal nodes of $cont$. Suppose that the p -rest of $cont$ with respect to η_i is $\langle rst_i, \chi_i, return-info_i \rangle$. Suppose that the instruction of η_i is $inst_i$. Then,

$$(E1) \quad cont(stg) = \bigcup \{ \text{execute } inst_i \text{ in } stg \text{ and } \langle rst_i, \chi_i, return-info_i \rangle \text{ followed} \\ \text{by doing } rst_i \text{ with the help of } \chi_i \text{ and } return-info_i \mid 1 \leq i \leq n \}.$$

Note, that each of the "execute $inst_i$..."s itself returns a set of answers so that it is necessary to take the union of these sets to get the desired set of answers for $cont(stg)$.

Suppose, now, that execution of $inst_i$ in stg and $\langle rst_i, \chi_i, return-info_i \rangle$ yields stg'_i and $cont'_i$. There are three ways in which the instruction may operate:

1. If $inst_i$ is value-returning and $return-info_i$ is non empty, then stg'_i is that explicitly yielded by the instruction and $cont'_i$ is rst_i as modified by passing up a value from the arc selected by χ_i according to $return-info_i$.
2. If $inst_i$ is value-returning and it calculates a new control tree, then $cont'_i$ that control tree and stg'_i is that explicitly yielded by the instruction.
3. If $inst_i$ is macro then stg'_i is stg and $cont'_i$ is obtained by hanging the macro subtree off the χ_i arc of rst_i leaving $return-info_i$ at the node selected by χ_i .

However $inst_i$ operates, the set of answers yielded by "execution of $inst_i$..." is that yielded by $cont'_i(stg'_i)$. Stated equationally,

$$(E2) \quad cont(stg) = \bigcup \{ \text{execute } inst_i \text{ in } stg \text{ and } \langle rst_i, \chi_i, return-info_i \rangle \text{ followed by} \\ \text{doing } rst_i \text{ with the help of } \chi_i \text{ and } return-info_i \mid 1 \leq i \leq n \} \\ = \bigcup \{ cont'_i(stg'_i) \mid 1 \leq i \leq n \}.$$

Following the pattern of the deterministic case, a meaning equation is built out of the last two elements of E2 rewritten as:

$$(E3) \quad \bigcup \{ \text{execute } inst_i \text{ in } stg \text{ and } \langle rst_i, \chi_i, return-info_i \rangle \text{ followed by doing} \\ rst_i \text{ with the help of } return-info_i \text{ and } \chi_i \\ \exists \text{ a terminal node } \eta_i \text{ of } cont \text{ such that } inst_i \text{ is the instruction of } \eta_i \\ \text{and } \langle rst_i, \chi_i, return-info_i \rangle \text{ is the } p\text{-rest of } cont \text{ w.r.t. } \eta_i \} \\ = \bigcup \{ cont'_i(stg'_i) \mid \exists \text{ a terminal node } \eta_i \text{ of } cont \text{ such that } inst_i \text{ is the} \\ \text{instruction of } \eta_i, \langle rst_i, \chi_i, return-info_i \rangle \text{ is the } p\text{-rest of } cont \\ \text{w.r.t. } \eta_i, \text{ and execution of } inst_i \text{ in } stg \text{ and } \langle rst_i, \chi_i, return-info_i \rangle \\ \text{yields } cont'_i \text{ and } stg'_i \}.$$

4.5 Meaning

The concern, then, is with the meaning of a *set* of instructions (from the terminal nodes of a control tree *cont*) in *stg* and a corresponding *set* of *p-rests* (of *cont* with respect to the terminal nodes). Parts of the previous sentences are parenthesized because a meaning should be defined for any set of instructions and any set of the same size of *p-rests*. However, the meaning makes sense only when the instructions are all from nodes of a single control tree, and the *p-rests* are all of that control tree with respect to the same nodes.

To keep the correspondence between elements of the two sets, it is convenient to make the two sets sequences of the same length; the *i*th instruction is executed in *stg* and the *i*th *p-rest*.

On this basis, it is possible to introduce a meaning function with functionality.*

$$M \in \text{Inst}^* \times (\text{is-ct} \times \text{is-sel} \times \text{is-ri})^* \times \text{is-atg-P(A)}$$

defined by

$$(E4) \quad M[\langle \text{Inst}_1, \dots, \text{Inst}_n \rangle] \langle \langle \text{rst}_1, \chi_1, \text{return-info}_1 \rangle, \dots, \langle \text{rst}_n, \chi_n, \text{return-info}_n \rangle \rangle \text{stg} \\ = \bigcup \{ \text{cont}'_i(\text{stg}'_i) \mid \text{execution of Inst}_i \text{ in stg and } \langle \text{rst}_i, \chi_i, \text{return-info}_i \rangle \text{ yields stg}'_i \text{ and cont}'_i \}$$

where it is assumed that there exists a control tree, *cont*, such that

1. $\{\eta_1, \dots, \eta_n\}$ = the set of terminal nodes of *cont*, and
2. for $1 \leq i \leq n$, *Inst*_{*i*} is the instruction of η_i , and $\langle \text{rst}_i, \chi_i, \text{return-info}_i \rangle$ is the *p-rest* of *cont* with respect to η_i .

The consistency assumption attached to E4 is to insure that the various pieces making up an instance of the equation could appear in a control tree together.

Each element of the set to which the generalized union operator is applied in E4 is a set of answers. Thus,

$$(E5) \quad M[\langle \text{Inst}_1, \dots, \text{Inst}_n \rangle] \langle \langle \text{rst}_1, \chi_1, \text{return-info}_1 \rangle, \dots, \langle \text{rst}_n, \chi_n, \text{return-info}_n \rangle \rangle \text{stg} \\ = \bigcup \{ \text{anset}_i \mid 1 \leq i \leq n \}$$

where *anset*_{*i*} is determined as follows:

*anset*_{*i*} = if execution of *Inst*_{*i*} in *stg* and $\langle \text{rst}_i, \chi_i, \text{return-info}_i \rangle$ gives rise to a value-returning group yielding *stg*'_{*i*} and *val*'_{*i*}

then

$$M[\langle \text{Inst}'_1, \dots, \text{Inst}'_h \rangle] \langle \langle \text{rst}'_1, \chi'_1, \text{return-info}'_1 \rangle, \dots, \langle \text{rst}'_h, \chi'_h, \text{return-info}'_h \rangle \rangle \text{stg}'_i$$

where *cont*' is *rst*_{*i*} with *val*'_{*i*} passed up from χ_i according to *return-info*_{*i*},

**is-ct* is the set of control trees, *is-sel* is the set of compound selectors, and *is-ri* is the set of *return-infos*.

$\{\eta_1', \dots, \eta_h'\}$ = terminal nodes of $cont'$,
 for $1 \leq j \leq h$, $inst_j'$ is the instruction of η_j' , and
 $\langle rst_j', \chi_j', return-info_j' \rangle$ is the p -rest of $cont'$ w.r.t. η_j'
elif execution of $inst_i$ in stg and $\langle rst_i, \chi_i, return-info_i \rangle$ gives rise
 to a value-returning group yielding stg_i' and $cont_i'$, and $return-info_i$ is Ω
then
 $M[\langle inst_1', \dots, inst_k' \rangle] \langle \langle rst_1', \chi_1', return-info_1' \rangle, \dots, \langle rst_k', \chi_k', return-info_k' \rangle \rangle stg_i'$
where $\{\eta_1', \dots, \eta_k'\}$ = terminal nodes of $cont'$,
 for $1 \leq j \leq k$, $inst_j'$ is the instruction of η_j' , and
 $\langle rst_j', \chi_j', return-info_j' \rangle$ is the p -rest of $cont'$ w.r.t. η_j'
elif execution of $inst_i$ in stg and $\langle rst_i, \chi_i, return-info_i \rangle$ gives rise
 to a macro group yielding a control subtree st
then
 $M[\langle inst_1', \dots, inst_m' \rangle] \langle \langle rst_1', \chi_1', return-info_1' \rangle, \dots, \langle rst_m', \chi_m', return-info_m' \rangle \rangle stg$
where $cont'$ is rst_i with st made its χ_i th component,
 $\{\eta_1', \dots, \eta_m'\}$ = terminal nodes of $cont'$,
 for $1 \leq j \leq m$, $inst_j'$ is the instruction of η_j' , and
 $\langle rst_j', \chi_j', return-info_j' \rangle$ is the p -rest of $cont'$ w.r.t. η_j'
fi
co Note the lack of prime after the last "stg" just before the last "where" **co**

The major reason for the complexity of E5 is to insure that interleaving of value-returning instructions is carried out to the fullest degree implied by the nondeterminism and parallelism. E5 has taken care that if a macro instruction being interleaved with A_1, \dots, A_n expands into an interleaving of B_1, \dots, B_k , then all of $A_1, \dots, A_n, B_1, \dots, B_k$ are interleaved together. If care is not taken, it is easy to end up with only the B_1, \dots, B_k being interleaved and then when they are all done, the interleaving of A_1, \dots, A_n continuing. The former is a correct model of the nondeterminism and parallelism while the latter is not.

For a particular VDL definition of the correct form with, say, k different instructions, $INST_1, \dots, INST_k$, E5 would be rewritten as E6 below:

$$(E6) \quad M[\langle inst_1, \dots, inst_n \rangle] \langle \langle rst_1, \chi_1, return-info_1 \rangle, \dots, \langle rst_n, \chi_n, return-info_n \rangle \rangle stg = \bigcup \{anset_i | 1 \leq i \leq n\}$$

where $anset_i =$

```

case insti in
  INST1 then M[[si1]]spr1 stg1'
  .
  .
  .
  INSTk then M[[sik]]sprk stgk'
esac

```

where each si_j is a sequence of instructions, and each spr_j is a like-lengthed sequence of p -rests. $M[[si_j]]spr_j stg_j'$ is the expression obtained by carrying out the conditional expression in E5 for $anset_i$ with

$inst_i = INST_j$.

It is necessary to observe that the formula E6 is an equation *scheme* rather than an equation as is normally given in a denotational semantic definition. It is an equation scheme and not an equation because of its dependence on the n particular instructions that happen to be at the terminal nodes of the current control tree and the n particular p -rests that happen to result when each of the n terminal nodes is removed from the tree, i.e., because of its dependence on the current control tree. There is one instance of this equation scheme, i.e., one equation, for each possible control tree.

In essence, the formula is an equation scheme representing a definition with an unbounded number of equations, which are finitely specifiable by the scheme.

It is interesting to note that there is yet another finite specification for this unbounded set of equations, namely the original VDL definition from which the scheme is constructed. The VDL definition, being in fact a program, is probably less hairy to the reader than the scheme, which is in a non-direct* form to permit the use of continuations or continuation-like objects.

The above equation scheme is messy, but it is in a form in which the meaning of a construct is given in terms of pieces of the control trees, i.e., p -rests, which state what is to be done after the execution of the current construct is finished. The next Section obtains the functions encoded by these control tree pieces.

From this informal development, it seems clear that a p -rest of a control tree, that is a $\langle rst, \chi, return-info \rangle$ triple plays the same role for a nondeterministic computation as does an $\langle rst, return-info \rangle$ pair or a continuation for a deterministic computation in that the p -rest tells how to proceed from the current storage and returned value to the final answers. Whereas the $\langle rst, return-info \rangle$ pair or the continuation yields a unique answer for any given value, environment, and storage, the p -rest in general does not yield a unique answer; it yields for any given value and storage a set of answers.

This argument suggests that whatever function a p -rest encodes, it should be on $V \times S$ to $\mathcal{P}(A)$. The next Section shows that the formal construction of such a function from a given p -rest. The domain of such functions will be called \mathcal{P} -Cont for Parallel Continuation, and a typical element of this domain is called \mathfrak{P} .

5. Formal development of parallel continuations

The development of this Section follows that of Section 3 and in fact assumes the definitions of that Section except as modified herein. In fact, this Section effectively begins at the end of Subsection 3.1 after which the restriction to deterministic control trees is introduced. Additionally, the present development takes into account that the environment has been moved out of the state and into the instruction argument lists.

* in the sense of direct as opposed to continuation semantics

* \mathfrak{P} is the Hebrew letter Peh.

5.1 Control Tree Representations

Since a node may have a set of successor nodes, a control tree representation may now be either an

1. *instr* or an
2. *instr*;
succ-set.

with the object represented by *instr* being as before. The object represented by *instr*; *succ-set* is

$$\mu(\text{instr}; \{ \langle \text{sel}(x, \text{succ-set}):x \rangle \mid x \in \text{succ-set} \})$$

where *instr* and *succ* represent objects as before, and *sel*(*x*, *set*) defines a one-to-one mapping from objects *x* ∈ *set* to selectors in R.

A group e.g.,

$$\text{group}_i(x_1, \dots, x_n, \xi),$$

may be in one of two forms

1. value returning:

$$\begin{aligned} & \text{PASS}:\epsilon_v(x_1, \dots, x_n, \xi) \\ & \text{s-stg}:\epsilon_\sigma(x_1, \dots, x_n, \xi) \\ & \text{s-c}:\epsilon_D(x_1, \dots, x_n, \xi) \end{aligned}$$

where each ϵ_i is an arbitrary expression; any of the lines may be missing; and a missing first (PASS) line is taken as equivalent to PASS:Ω

2. macro:

$$\text{ct-rep}(x_1, \dots, x_n, \xi)$$

a control tree representation.

Δ_n can now be defined as

$$\begin{aligned} \Delta_n(x_1, \dots, x_m, \tau, r_i) = & \\ & p_1(x_1, \dots, x_n, \xi) \rightarrow \text{group}_1^+(x_1, \dots, x_n, \xi, \tau, r_i) \\ & \cdot \\ & \cdot \\ & p_m(x_1, \dots, x_n, \xi) \rightarrow \text{group}_m^+(x_1, \dots, x_n, \xi, \tau, r_i) \end{aligned}$$

where $group_i^+$ is obtained from $group_i$ according to the latter's form:

1. value returning:

$$\langle \mu(\mu(\xi; \{ \langle s-comp(ri) \circ elem(j) \circ s-al(pred^i(\tau)) \circ s-c: \epsilon_v(x_1, \dots, x_n, \xi) \rangle \mid \langle i, j \rangle \in s-ap(ri) \})), \langle s-stg: \epsilon_\sigma(x_1, \dots, x_n, \xi) \rangle, \langle s-c: \epsilon_D(x_1, \dots, x_n, \xi) \rangle, \epsilon_v(x_1, \dots, x_n, \xi) \rangle$$

2. macro:

$$\langle \mu(\xi; \langle \tau \circ s-c: \mu(ct-rep(x_1, \dots, x_n, \xi); \langle s-ri: ri \rangle) \rangle), \Omega \rangle$$

With these definitions it is possible to define a function $p-cont$ on $p-rests$ to $P-Conts$,

$$p-cont: is-ct \times is-sal \times is-ri \rightarrow P-Cont$$

which given the components of a $p-rest$, yields a $P-Cont$, \mathcal{B} , such that

$$\mathcal{B}: V \times S \rightarrow \mathcal{P}(A).$$

Figure 9 is useful for seeing where the pieces of the definition of $p-cont$ come from.

In the definition the formal parameters of $p-cont$, ct , τ , and ri together constitute a $p-rest$. The result is a function with formal parameters $\epsilon \in V$ and $\sigma \in S$ which, as can be seen, returns a set of answers.

Definition 2. Suppose $is-c(ct)$ and $is-ri(ri)$. Suppose $tn(ct)$ yields a set, i.e., ct is nondeterministic. Suppose $\tau \in tn(ct)$.

Then,

$$p-cont(ct, \tau, ri) = \lambda(\epsilon, \sigma). \begin{aligned} & (is-\Omega(ct) \rightarrow \{\sigma\}, \\ & T \rightarrow \bigcup \{ p-cont(ct_i', pred(\tau_i, nri_i)(\epsilon_i', \sigma_i')) \mid 1 \leq i \leq m \} \end{aligned}$$

where

$$\begin{aligned} ct^\dagger &= \mu(ct; \{ \langle s-comp(ri) \circ elem(j) \circ s-al(pred^{i-1}(\tau)) : \epsilon \rangle \mid \langle i, j \rangle \in s-ap(ri) \} \\ \{\tau_1, \dots, \tau_m\} &= tn(ct^\dagger) \\ \text{for each } i \in \{j \mid 1 \leq j \leq m\}, \\ node_i &= \tau_i(ct^\dagger) \\ nln_i &= s-in(node_i), \\ nal_i &= s-al(node_i), \\ nri_i &= s-ri(node_i), \end{aligned}$$

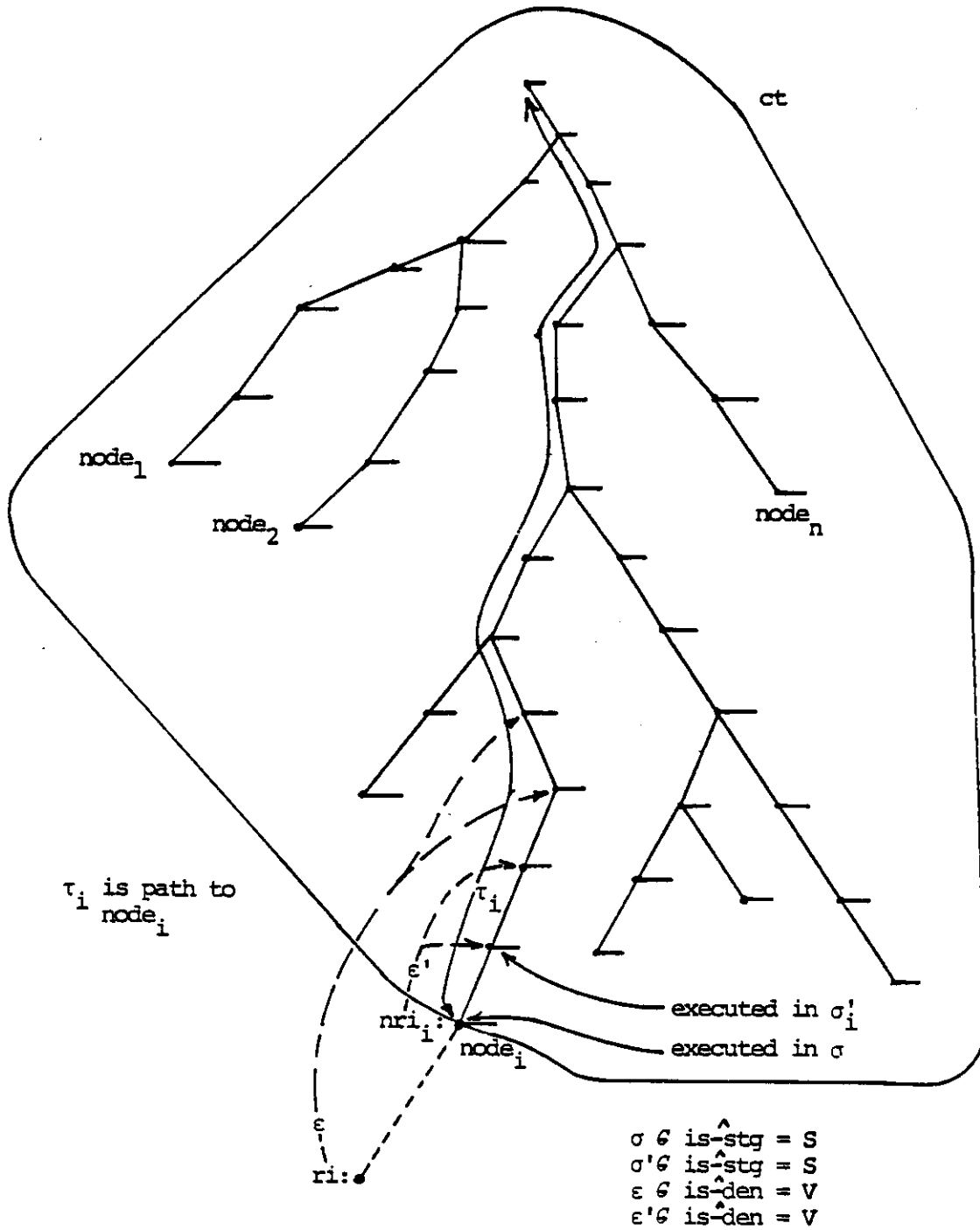


Figure 9

$$\begin{aligned}
n_i &= \text{number of formal parameters in schema for } s\text{-in}(node_i) \\
\langle \mu_o(\langle s\text{-stg}:\sigma_i' \rangle, \langle s\text{-c}:ct_i' \rangle), \epsilon_i' \rangle &= \\
&\Delta_{nr_i}(elem(1, al_i), \dots, elem(n_i, al_i), \\
&\mu_o(\langle s\text{-stg}:\sigma \rangle, \langle s\text{-c}:\delta(ct_i, \tau_i) \rangle), \tau_i, nr_i)
\end{aligned}$$

6. Attempt to Use P-continuations in Equations

Thus, it is possible given a p -rest to construct the parallel continuation it encodes. The question now arises as to whether the nondeterministic meaning equation E5 of Section 4.5 can be recast into a form in which the corresponding parallel continuation can be used in place of each p -rest. After all, denotational continuation semantics for deterministic computations does use a continuation wherever the equations of this paper use a $\langle rst, return\text{-info} \rangle$ pair.

The answer seems to be "No!". The control tree of a p -rest carries information which is lost when its parallel continuation is extracted. Both convey the set of all possible final answers starting from a particular (single) storage and a particular (single) value. However, only the control tree also exhibits what will be the set of terminal nodes to choose from following the selection and execution of one of its terminal nodes. This information is needed to construct the p -rest of the rst of the current p -rest with respect to each of the resulting terminal nodes. *Only with this information can macro expansion result in an interleaving of the nodes of the appended subtree with those of the original rst .*

Therefore, it seems that the meaning equation must be stated in terms of p -rests which explicitly encode the nondeterministic choices and that a parallel condition cannot be used in the equation and maintain completely interleaved nonindependent parallelism. It is interesting though, that from any such p -rest, the parallel continuation it encodes is obtainable.

This loss of information in going from control tree to continuation is not critical in the deterministic case because there is only one choice at any time, and thus, it is not necessary to encode choices.

As a consequence, the semantics for parallelism proposed in this paper is a continuation semantics only in an indirect manner.

Is the semantics denotational in the sense that the meaning of a syntactic construct is constructible from those of its direct syntactic components? If one extends the notion of syntactic construct to include a set of them, then the answer is "Yes!" The meaning equation says that the meaning of a sequence (really, a set) of syntactic entities is constructed from the meanings of the direct components of the individual elements of the sequence (really, the set).

The reader should observe the similarity between the p -rests and Plotkin's resumptions. In both cases, the transition from a state yields a set of possible next states plus p -rests or resumptions, as the case may be. Both carry enough information to continue in the same manner from any of the possible next states. Plotkin and Smyth also observe that the more abstract functions on states to sets of states, called P-continuations in the present paper, are not powerful enough to model full interleaving. The present paper, however, shows how to obtain the more abstract functions from the less abstract p -rests.

6.1 Alternative Meaning and Interpreter

It is thus recognized that the proposed semantic definition for nondeterminism and parallelism is only denotational and cannot be made also continuation. An opportunity then arises to abandon the non-direct form of the meaning equation scheme, which has various nonindependent pieces of one control tree on both sides and for which there must be integrity constraints guaranteeing that the pieces could come from one single tree. This non-direct form was used from the beginning to permit eventual replacement of the *p-rests* by parallel continuations. With the possibility of this replacement scuttled, perhaps a more direct form of a meaning equation scheme can be used in which the meaning function has a single whole control tree as a parameter:

$$M' \in \text{is-ct} \times \text{is-stg} \rightarrow \mathcal{P}(A).$$

Equation E6 defining

$$M'[\langle \text{inst}_1, \dots, \text{inst}_n \rangle] \ll \langle \text{rst}_1, \chi_1, \text{return-info}_1 \rangle, \dots, \langle \text{rst}_n, \chi_n, \text{return-info}_n \rangle \gg \text{stg}$$

can be revised to define

$$M'[\text{cont}] \text{stg}$$

where *cont* is the control tree with terminal nodes $\{\eta_1, \dots, \eta_n\}$ such that for $1 \leq i \leq n$, *inst_i* is the instruction of η_i and $\langle \text{rst}_i, \chi_i, \text{return-info}_i \rangle$ is the *p-rest* of *cont* with respect to η_i . Specifically

$$(E7) \quad M'[\text{cont}] \text{stg} = \bigcup \{ \text{anset}_i \mid 1 \leq i \leq n \}$$

where *anset_i* is determined as follows:

anset_i = if execution of *inst_i* in *stg* and $\langle \text{rst}_i, \chi_i, \text{return-info}_i \rangle$ gives rise to a value-returning group yielding *stg_i'* and *val_i'*
then
 $M'[\text{cont}'_i] \text{stg}'_i$
where *cont'* is *rst_i* with *val_i'* passed up from χ_i according to *return-info_i*,
elif execution of *inst_i* in *stg* and $\langle \text{rst}_i, \chi_i, \text{return-info}_i \rangle$ gives rise to a value-returning group yielding *stg_i'* and *cont_i'*, and *return-info_i* is Ω
then
 $M'[\text{cont}'_i] \text{stg}'_i$
elif execution of *inst_i* in *stg* and $\langle \text{rst}_i, \chi_i, \text{return-info}_i \rangle$ gives rise to a macro group yielding a control subtree *st*
then
 $M'[\text{cont}'_i] \text{stg}$
where *cont'* is *rst_i* with *st* made its χ_i th component
fi

This new meaning function is cleaner than the original but is still only an equation scheme. This meaning function is essentially the interpreter, and its most direct finite specification is the VDL definition itself!

6.2 Denotation of Parallel Programs

If one chooses to write a denotational definition of the language using the equation scheme suggested by E6, it is legitimate to ask, "Just what is the denotation of a program?" Because of the existence of the function $p\text{-cont}$ yielding the element of $P\text{-Cont}$ encoded by any given $p\text{-rest}$, there are two possible answers to this question. The denotation of a program can be taken either as a $p\text{-rest}$ or as a parallel continuation.

Given a program p , the $p\text{-rest}$ denoting p is*

$$\langle \text{interpret-program}(p), I, \Omega \rangle,$$

and the parallel continuation denoting p is

$$p\text{-cont}(\text{interpret-program}(p), I, \Omega).$$

Each of these applied (in its own way) to an initial storage stg_0 and an empty value yields the set of answers resulting from executing p with an initial storage stg_0 .

If one insists that whatever is taken as the denotation be useable in an equation scheme of the form suggested by E6 and that the denotation of a construct be constructible from the *same kind* of denotation of its direct syntactic components, then only the $p\text{-rest}$ denotation can be used.

In either case, the denotation of a program and its set of final answers depends on the choice of which operations are indivisible, i.e., value-returning in VDL. It is these indivisible operations that get interleaved to produce a computation sequence. A different choice of indivisible operations yields a different set of possible interleavings and thus a different set of final answers.

Note that the denotation of $p:=p+1;p:=p+1$,

$$\langle \text{interpret-st-llst}(\langle p:=p+1, p:=p+1 \rangle), I, \Omega \rangle,$$

is different from that of $p:=p+2$,

$$\langle \text{interpret-st}(p:=p+2), I, \Omega \rangle.$$

However, this is as it should be, because in the presence of other processes accessing the same memory, the results of the two program fragments could very well be different.

There might be objections to tying the definition of a programming language to such an implementation dependent concept as the set of indivisible operations. However,

1. ultimately, in any computational system permitting shared access of a common storage medium, there is a smallest, indivisible operation that cannot be interrupted -- usually the assignment to a single word or byte. (Without this shared access to a storage medium, parallelism is uninteresting and poses no problems; the processes are completely independent and can be defined completely separately.)
2. all programming languages known to this author assume that certain operations, especially assignment to a single variable, are indivisible. Even Algol 68, whose definition [vWn75] says explicitly

* I is the identity selector.

that which actions are inseparable (indivisible) is left undefined and thus up to the implementation, ends up specifying *effectively indivisible* operations [Sch78]. There are other axioms in the definition that allow deducing that even if assignation of a single variable is not indivisible in fact in an implementing machine, it must be implemented as if it were.

3. at the language level, synchronization is used to make long sequences of operations effectively indivisible. This fact carries the implication of the existence of some indivisible operations, e.g., incrementation and test-and-set, with which the synchronization primitives can be implemented.

The final technical question is whether the recursive function definitions given in Sections 3 and 5 of `cont` and `p-cont` have fixed points. This author is not in a position to answer this question and welcomes anyone to consider the question. He feels however that the definitions do define functions simply because he knows that the VDL functions on which they are based work and the notion of a computation as a sequence of states generalized by Λ works. These new functions capture a whole computation as a recursive rendition of the loop:

```
while  $\exists$  an instruction to execute do
  select one instruction inst;
  determine set of possible next instructions;
  execute inst
od.
```

7. Conclusion

This paper has presented a denotational semantics for nondeterminism and parallelism which is at once

1. powerful enough to deal with arbitrary interleaved access to shared memory,
2. systematically constructible from an operational semantics of the same, and
3. such that a more abstract functional meaning on states to sets of states is obtainable from the given meaning.

This paper has also dealt with the relation between VDL and denotational semantics. The conclusion is that from a technical point of view, it does not matter whether a VDL or a denotational semantic definition is given of a programming language. Section 3 shows how to construct a denotational continuation semantic definition from a deterministic VDL semantic definition. Appendix IV gives a VDL definition constructed from the example denotational continuation semantic definition found in Chapter 13 of [Ten81]. Because each can be constructed from each other, both are based on the same firm mathematical grounds. It becomes strictly a matter of taste as to which style is used. A language definer should take into account which is easier for him or her to write and which is easier for the intended audience to use in the intended manner.

One supposed advantage of denotational semantics is that it is easily used in proofs of properties about the defined language. However, the fact is that operational and especially VDL definitions have been used in proofs also [HJ70, JL71, McG70, McG72, Bry72]. A favorite exercise is to demonstrate the correctness of an implementation of a language or a feature by showing the implementation operationally equivalent to the definition. In general, any property may be proved by a computational induction that shows it true in all states of a computation.

It is clear that the mutual constructibility extends also to any formal presentation of operational semantics. Consider the most recent (to this author's knowledge) presentation of operational semantics developed by Hennessy, Li, and Plotkin (See e.g., [Pl083]). A specification in their structured operational semantics can be systematically converted to denotational form either directly or via VDL. The rules

$$\begin{array}{l} \langle \text{skip}, \sigma \rangle \rightarrow \sigma \\ \frac{\langle y, \sigma \rangle \rightarrow v}{\langle x := y, \sigma \rangle \rightarrow \sigma_v^x} \\ \frac{\langle c_0, \sigma \rangle \rightarrow \sigma'}{\langle c_0; c_1 \rangle \rightarrow \langle c_1, \sigma' \rangle} \end{array}$$

can be expressed in the denotational equations as they suggest,

$$\begin{array}{l} C[\text{skip}]\sigma = \sigma \\ C[x := y]\sigma = \sigma_v^x \\ \quad \text{where } C[y]\sigma = v \\ C[c_0; c_1]\sigma = C[c_0] \circ C[c_1]\sigma . \end{array}$$

Alternatively, the rules may be converted into a VDL instruction for a machine with only a control tree,*

$$\begin{array}{l} \text{interpret}(c, \sigma) = \\ c = [\text{skip}] - \\ \quad \text{pass}(\sigma) \\ c = [x := y] - \\ \quad \text{pass}(\mu(\sigma; \langle x: v \rangle)); \\ \quad \quad v: \text{interpret}(y, \sigma) \\ c = [c_0; c_1] - \\ \quad \text{interpret}(c_1, \sigma'); \\ \quad \quad \sigma': \text{interpret}(c_0, \sigma) \end{array}$$

and then to almost the same denotational equations.

Finally, this paper has observed that VDL control trees really encode continuations. One may note that the control tree idea is even more prevalent than just in VDL definitions. Examination of Section 2.1.4, *Actions*, of the Revised Algol 68 Report [vWn75] shows that the interpreter, described in English, is really a control tree with actions at each node. An action is the elaboration (execution) of a construct (piece of program text) in an environ (environment). This elaboration may both have an effect on the state and yield a value. Furthermore, examination of Sections 1.2.3 through 1.2.5, on operations, instructions, and the mechanization of the meta-language, of the ECMA/ANSI PL/I BASIS/I definition [ANSI74] shows that the PL/I interpretation machine also has a control tree, namely a parse tree of instructions in the process of being executed. By a suitable systematic construction, these two definitions may be converted to denotational semantic definitions.

*Lest the reader draw the conclusion that VDL produces longer definitions, observe that short instruction names e.g., "C" or "<" could have been used as well.

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Appendix I

Abstract Syntax of Program:

- (A1) $is-progr = is-block$
- (A2) $is-block = (\langle s-decl-part:is-decl-part \rangle, \langle s-st-list:is-st-list \rangle)$
- (A3) $is-decl-part = (\{\langle id:is-attr \rangle \parallel is-id(id)\})$
- (A4) $is-attr = is-var-attr \vee is-proc-attr \vee is-funct-attr \vee is-label-attr$
- (A5) $is-var-attr = \{INT, LOG, LABVAR\}$
- (A6) $is-proc-attr = (\langle s-param-list:is-id-list \rangle, \langle s-st:is-st \rangle)$
- (A7) $is-funct-attr = (\langle s-param-list:is-id-list \rangle, \langle s-st:is-st \rangle, \langle s-expr:is-expr \rangle)$
- (A7.5) $is-label-attr = is-st-list$
- (A8) $is-st = is-assign-st \vee is-cond-st \vee is-proc-call \vee is-block \vee is-goto-st \vee is-while-st$
- (A9) $is-assign-st = (\langle s-left-part:is-var \rangle, \langle s-right-part:is-expr \rangle)$
- (A10) $is-expr = is-cont \vee is-var \vee is-funct-des \vee is-bin \vee is-unary$
- (A11) $is-const = is-log \vee is-int$
- (A12) $is-var = is-id$
- (A13) $is-funct-des = (\langle s-id:is-id \rangle, \langle s-arg-list:is-id-list \rangle)$
- (A14) $is-bin = (\langle s-rd1:is-expr \rangle, \langle s-rd2:is-expr \rangle, \langle s-op:is-bin-rt \rangle)$
- (A15) $is-unary = (\langle s-rd:is-expr \rangle, \langle s-op:is-unary-rt \rangle)$
- (A16) $is-cond-st = (\langle s-expr:is-expr \rangle, \langle s-then-st:is-st \rangle, \langle s-else-st:is-st \rangle)$
- (A17) $is-proc-call = (\langle s-id:is-id \rangle, \langle s-arg-list:is-id-list \rangle)$
- (A18) $is-goto-st = id-id$
- (A19) $is-while-st = (\langle s-cond:is-expr \rangle, \langle s-body:is-st \rangle)$

where:

- $is-id$ an infinite set of identifiers
- $is-log$ a set of constants denoting the truth values
- $is-int$ an infinite set of constants denoting the integer values
- $is-unary-rt$ a set of unary (one-place) operators
- $is-binary-rt$ a set of binary (two-place) operators
- $\{INT, LOG\}$ two attributes used to distinguish integer variables from logical variables.

Abstract Syntax of State:

- (S1) $is-state = (\langle s-env:is-env \rangle, \langle s-c:is-c \rangle, \langle s-at:is-at \rangle, \langle s-dn:is-dn \rangle, \langle s-d:is-d \rangle, \langle s-n:is-integer-value \rangle)$
- (S2) $is-env = (\{\langle id:is-n \rangle \parallel is-id(id)\})$
- (S3) $is-c = \dots$ (standard control trees as defined in LLS70)
- (S4) $is-at = (\{\langle n:is-type \rangle \parallel is-n(n)\})$
- (S5) $is-type = \{INT, LOG, PROC, FUNCT, LABVAR, LABCONST\}$
- (S6) $is-dn = (\{\langle n:is-value \vee is-proc-den \vee is-funct-den \vee is-label-den \rangle \parallel is-n(n)\})$
- (S6.1) $is-proc-den = (\langle s-env:is-env \rangle, \langle s-attr:is-proc-attr \rangle)$

- (S6.2) $is\text{-}funct\text{-}den = (\langle s\text{-}env:is\text{-}env \rangle, \langle s\text{-}attr:is\text{-}funct\text{-}attr \rangle)$
 (S6.3) $is\text{-}label\text{-}den = (\langle s\text{-}env:is\text{-}env \rangle, \langle s\text{-}c:is\text{-}c \rangle, \langle s\text{-}d:is\text{-}d \rangle)$
 (S7) $is\text{-}d = (\langle s\text{-}env:is\text{-}env \rangle, \langle s\text{-}c:is\text{-}c \rangle, \langle s\text{-}d:is\text{-}d \rangle) \vee is\text{-}\Omega$

where:

$is\hat{=n}$ infinite set of names (used for the generation of unique names)

{PROC, two attributes used to distinguish function names and procedure names

{s-env, s-c, s-at, s-dn, s-n} selectors for the components of the interpreting machine

is-vâalue infinite set of values, the integers and the truth values

The *initial state* for any given program $t \in is\text{-}p\hat{=}rgr$ is:
 $\mu_0(\langle s\text{-}c:int\text{-}p\hat{=}rgr(t) \rangle, \langle s\text{-}n:1 \rangle)$

States ξ whose control part $s\text{-}c(\xi)$ is Ω are *end states*.

Abbreviations used in Instruction Schemata:

ENV $s\text{-}env(\xi)$
 C $s\text{-}c(\xi)$
 AT $s\text{-}at(\xi)$
 DN $s\text{-}dn(\xi)$
 D $s\text{-}d(\xi)$

Instruction Schemata:

- (I1) $int\text{-}p\hat{=}rgr(t) = int\text{-}block(t)$
 for: $is\text{-}p\hat{=}rgr(t)$
- (I2) $int\text{-}block(t) =$
 $s\text{-}d:\mu_0(\langle s\text{-}env:ENV \rangle, \langle s\text{-}c:C \rangle, \langle s\text{-}d:D \rangle)$
 $s\text{-}c:exit;$
 $int\text{-}st\text{-}list(s\text{-}st\text{-}list(t));$
 $int\text{-}decl\text{-}part(s\text{-}decl\text{-}part(t));$
 $update\text{-}env(s\text{-}decl\text{-}part(t))$
 for: $is\text{-}block(t)$
- (I3) $update\text{-}env(t) =$
 null;
 $\{update\text{-}id(id, n); n:un\text{-}name \mid id(t) \neq \Omega\}$
 for: $is\text{-}decl\text{-}part(t)$

- (I4) $\text{update-ld}(id, n) =$
 $\quad s\text{-env}:\mu(\text{ENV}; \langle id:n \rangle)$
for: $\text{is-id}(id)$, $\text{is-n}(n)$
- (I5) $\text{int-decl-part}(t) =$
 $\quad \text{null};$
 $\quad \{ \text{int-decl}(id(\text{ENV}), id(t)) \mid id(t) \neq \Omega \}$
for: $\text{is-decl-part}(t)$
- (I6) $\text{int-decl}(n, attr) =$
 $\quad \text{is-var-attr}(attr) \rightarrow$
 $\quad \quad s\text{-at}:\mu(\text{AT}; \langle n:attr \rangle)$
 $\quad \text{is-proc-attr}(attr) \rightarrow$
 $\quad \quad s\text{-at}:\mu(\text{AT}; \langle n:\text{PROC} \rangle)$
 $\quad \quad s\text{-dn}:\mu(\text{DN}; \langle n:\mu_o(\langle s\text{-attr}:attr \rangle, \langle s\text{-env}:\text{ENV} \rangle) \rangle)$
 $\quad \text{is-funct-attr}(attr) \rightarrow$
 $\quad \quad s\text{-at}:\mu(\text{AT}; \langle n:\text{FUNCT} \rangle)$
 $\quad \quad s\text{-dn}:\mu(\text{DN}; \langle n:\mu_o(\langle s\text{-attr}:attr \rangle, \langle s\text{-env}:\text{ENV} \rangle) \rangle)$
 $\quad \text{is-label-attr}(attr) \rightarrow$
 $\quad \quad s\text{-at}:\mu(\text{AT}; \langle n:\text{LABCONST} \rangle)$
 $\quad \quad s\text{-dn}:\mu(\text{DN}; \langle n:\mu_o(\langle s\text{-env}:\text{ENV} \rangle, \langle s\text{-d}:D \rangle,$
 $\quad \quad \quad \langle s\text{-c}:exit;$
 $\quad \quad \quad \text{int-st-list}(attr); \rangle) \rangle)$
for: $\text{is-n}(n)$, $\text{is-attr}(attr)$
- (I7) $\text{int-st-list}(t) =$
 $\quad \text{is-}\langle \rangle(t)\text{-null}$
 $\quad \top \rightarrow$
 $\quad \quad \text{int-st-list}(\text{tail}(t));$
 $\quad \quad \text{int-st}(\text{head}(t))$
for: $\text{is-st-list}(t)$
- (I8) $\text{int-st}(t) =$
 $\quad \text{is-assign-st}(t) \rightarrow \text{int-assign-st}(t)$
 $\quad \text{is-cond-st}(t) \rightarrow \text{int-cond-st}(t)$
 $\quad \text{is-proc-call}(t) \& (at_1 = \text{PROC}) \rightarrow \text{int-proc-call}(t)$
 $\quad \text{is-block}(t) \rightarrow \text{int-block}(t)$
 $\quad \text{is-goto-st}(t) \& (at_1 = \text{LABCONST} \vee at_1 = \text{LABVAR}) \rightarrow \text{int-goto-st}(t)$
 $\quad \text{is-while-st}(t) \rightarrow \text{int-while-st}(t)$
where: $at_1 = ((s\text{-id}(t))(\text{ENV}))(\text{AT})$,
 $\quad at_1 = (t(\text{ENV}))(\text{AT})$
for: $\text{is-st}(t)$
- (I9) $\text{int-assign-st}(t) =$
 $\quad \text{is-var-attr}(n_1(\text{AT})) \rightarrow$
 $\quad \quad \text{assign}(n_1, v);$
 $\quad \quad v:\text{int-expr}(s\text{-right-part}(t))$
 $\quad \top \rightarrow \text{error}$
where: $n_1 = (s\text{-left-part}(t))(\text{ENV})$
for: $\text{is-assign-st}(t)$
- (I10) $\text{assign}(n, v) =$

```

      s-dn:μ(DN;n<:convert(v,n(AT))>)
for: is-n(n), is-value(v)

(I11) int-cond-st(t) =
      branch(v,s-then-st(t),s-else-st(t));
      v:int-expr(s-expr(t))
for: is-cond-st(t)

(I12) branch(v,st1,st2) =
      convert(v,LOG)-int-st(st1)
      -convert(v,LOG)-int-st(st2)
for: is-value(v), is-st(st1), is-st(st2)

(I13) int-proc-call(t) =
      (length(arg-listt) = length(p-listt))-
      s-env:μ(envt;{<elem(i,p-listt):elem(i,arg-listt)(ENV)> |
      1≤i≤length(p-listt)})
      s-d:μo(<s-env:ENV>,<s-c:C>,<s-d:D>)
      s-c:exit;
      int-st(stt)

      T-error
where: nt = (s-id(t))(ENV),
      p-listt = s-param-list * s-attr * nt(DN),
      envt = s-env * nt(DN),
      arg-listt = s-arg-list(t),
      stt = s-st * s-attr * nt(DN)
for: is-proc-call(t)

(I14) exit =
      s-env:s-env(D)
      s-c:s-c(D)
      s-d:s-d(D)

(I15) int-expr(t) =
      is-bin(t)-
      int-bin-op(s-op(t),a,b);
      a:int-expr(s-rd1(t)),
      b:int-expr(s-rd2(t))
      is-unary(t)-
      int-un-op(s-op(t),a);
      a:int-expr(s-rd(t))
      is-funct-des(t)&(stt = FUNCT)-
      pass-value(n);
      int-funct-call(t,n);
      n:un-name
      is-var(t)&is-var-attr(nt(AT))-
      PASS:nt(DN)
      is-const(t)-
      PASS:value(t)

      T-error
where: nt = t(ENV),
      stt = ((s-id(t))(ENV))(AT)

```

for: is-expr(t)

(I16) pass-value(n) =
PASS:n(DN)

(I17) Int-funct-call(t,n) =
(length(arg-list_t) = length(p-list_t))-
s-env:μ(env_t;{<elem(i,p-list_t):elem(i,arg-list_t)(ENV)> |
1 ≤ i ≤ length(p-list_t)})
s-d:μ_o(<s-env:ENV>, <s-c:C>, <s-d:D>)
s-c:exit;
assign(n,v);
v: Int-expr(expr_t);
Int-st(st_t)

T-error

where: n_t = (s-id(t))(ENV),
p-list_t = s-param-list * s-attr * n_t(DN),
env_t = s-env * n_t(DN),
arg-list_t = s-arg-list(t),
st_t = s-st * s-attr * n_t(DN),
expr_t = s-expr * s-attr * n_t(DN)

for: is-funct-des(t), is-n(n)

(I18) Int-goto-st(t) =
s-env:env_t
s-d:d_t
s-c:c_t
where: n_t = t(ENV),
env_t = s-env * n_t(DN),
d_t = s-d * n_t(DN),
c_t = s-c * n_t(DN)

for: is-goto-st(t)

(I19) Int-while-st(t) =
loop-or-exit(v,t);
v: Int-expr(s-cond(t))
for: is-while-st(t)

(I20) loop-or-exit(v,t) =
convert(v,LOG)-
Int-while-st(t);
Int-st(s-body(t))
-convert(v,LOG)-null
for: is-value(v), is-while-st(t)

where (defined):

un-name =
PASS:n_{s-n(ξ)}
s-n:s-n(ξ)+1

null =
PASS:Ω

where:

The following functions and instructions are not further specified:

convert(v,attr) function which yields v converted (if necessary) to the type specified by attr which may either be INT or LOG.

int-bin-op(op,a,b) Instruction which returns the result of applying the operator op to a and b. It is left open whether there is a conversion performed in case the operator is not applicable to operands of type a and b.

int-un-op(op,a) Instruction which returns the result of applying the operator op to a (for the problem of conversion see above).

value(a) Function which yields the value given a constant a.

Appendix II

Abstract Syntax of Program:

- (A1) is-progr = is-block
- (A2) is-block = (<s-decl-part:is-decl-list>, <s-st-list:is-st-list>)
- (A3) is-decl = (<s-id:is-id>, <s-attr:is-attr>)
- (A4) is-attr = is-var-attr \vee is-proc-attr \vee is-funct-attr \vee is-label-attr
- (A5) is-var-attr = {INT, LOG, LABVAR}
- (A6) is-proc-attr = (<s-param-list:is-id-list>, <s-st:is-st>)
- (A7) is-funct-attr = (<s-param-list:is-id-list>, <s-st:is-st>, <s-expr:is-expr>)
- (A7.5) is-label-attr = is-st-list
- (A8) is-st = is-assign-st \vee is-cond-st \vee is-proc-call \vee is-block \vee
is-goto-st \vee is-while-st
- (A9) is-assign-st = (<s-left-part:is-var>, <s-right-part:is-expr>)
- (A10) is-expr = is-cont \vee is-var \vee is-funct-des \vee is-bin \vee is-unary
- (A11) is-const = is-log \vee is-int
- (A12) is-var = is-id
- (A13) is-funct-des = (<s-id:is-id>, <s-arg-list:is-id-list>)
- (A14) is-bin = (<s-rd1:is-expr>, <s-rd2:is-expr>, <s-op:is-bin-rt>)
- (A15) is-unary = (<s-rd:is-expr>, <s-op:is-unary-rt>)
- (A16) is-cond-st = (<s-expr:is-expr>, <s-then-st:is-st>, <s-else-st:is-st>)
- (A17) is-proc-call = (<s-id:is-id>, <s-arg-list:is-id-list>)
- (A18) is-goto-st = id-id
- (A19) is-while-st = (<s-cond:is-expr>, <s-body:is-st>)

where:

- is- $\hat{i}d$ an infinite set of identifiers
- is- $\hat{l}og$ a set of constants denoting the truth values
- is- $\hat{i}nt$ an infinite set of constants denoting the integer values
- is- $\hat{u}n\hat{a}r\hat{y}\hat{r}t$ a set of unary (one-place) operators
- is- $\hat{b}i\hat{n}\hat{a}r\hat{y}\hat{r}t$ a set of binary (two-place) operators
- {INT, LOG} two attributes used to distinguish integer variables
from logical variables.

Abstract Syntax of State:

- (S1) is-state = (<s-env:is-env>, <s-c:is-c>, <s-stg:is-stg>)
- (S2) is-env = ({<id:is-n> | is-id(id)})
- (S3) is-c = ... (standard control trees as defined in LLS70)
- (S4) is-stg = ({<n:(<s-dn:is-den>, <s-at:is-type>) | is-n(n)>})
- (S5) is- $\hat{t}y\hat{p}e$ = {INT, LOG, PROC, FUNCT, LABVAR, LABCONST}
- (S6) is-den = is-proc-den \vee is-funct-den \vee is-value \vee
is-label-den \vee is-UNINIT
- (S6.1) is-proc-den = (<s-env:is-env>, <s-attr:is-proc-attr>)
- (S6.2) is-funct-den = (<s-env:is-env>, <s-attr:is-funct-attr>)

(S6.3) $is\text{-}label\text{-}den = (\langle s\text{-}env\text{-}is\text{-}env \rangle, \langle s\text{-}cis\text{-}c \rangle)$

where:

$is\text{-}n$	infinite set of names (used for the generation of unique names)
{PROC, FUNCT}	two attributes used to distinguish function names and procedure names
{s-env, s-c, s-at, s-dn, s-stg}	selectors for the components of the interpreting machine
$is\text{-}value$	infinite set of values, the integers and the truth values

The *initial state* for any given program $t \in is\text{-}progr$ is:
 $\mu_0(\langle s\text{-}c: int\text{-}progr(t) \rangle, \langle s\text{-}n: 1 \rangle)$

States ξ whose control part $s\text{-}c(\xi)$ is Ω are *end states*.

Abbreviations used in Instruction Schemata:

ENV	$s\text{-}env(\xi)$
C	$s\text{-}c(\xi)$
STG	$s\text{-}stg(\xi)$

Instruction Schemata

- (I1) $int\text{-}progr(t) = int\text{-}block(t)$
 for: $is\text{-}progr(t)$
- (I2) $int\text{-}block(t) =$
 $exit(ENV);$
 $int\text{-}st\text{-}list(s\text{-}st\text{-}list(t));$
 $int\text{-}decl\text{-}part(s\text{-}decl\text{-}part(t), C, ENV);$
 $update\text{-}env(s\text{-}decl\text{-}part(t))$
 for: $is\text{-}block(t)$
- (I3) $update\text{-}env(t) =$
 $is\text{-}\langle \rangle(t)\text{-}null$
 T-
 $update\text{-}env(tail(t));$
 $update\text{-}id(s\text{-}id(head(t)), n);$
 n: $un\text{-}name$
 for: $is\text{-}decl\text{-}part(t)$
- (I4) $update\text{-}id(id, n) = s\text{-}env: \mu(ENV; \langle id: n \rangle)$

for: is-id(id), is-n(n)

(15) **int-decl-part(t,outerct,outerenv) =**
 is-<>(t)-null
 T→
 int-decl-part(tail(t),outerct,outerenv);
 int-decl(s-id(head(t))(ENV),s-attr(head(t)),outerct,outerenv);
 for: is-decl-part(t), is-c(outerct), is-env(outerenv)

(16) **int-decl(n,attr,outerct,outerenv) =**
 is-var-attr(attr)→
 s-stg:μ(STG;
 <n:μ_o(<s-at:attr>
 <s-dn:UNINIT>)>)
 is-proc-attr(attr)→
 s-stg:μ(STG;
 <n:μ_o(<s-at:PROC>
 <s-dn:μ_o(<s-attr:attr>
 <s-env:ENV>)>>)
 is-funct-attr(attr)→
 s-stg:μ(STG;
 <n:μ_o(<s-at:FUNCT>
 <s-dn:μ_o(<s-attr:attr>
 <s-env:ENV>)>>)
 is-label-attr(attr)→
 s-stg:μ(STG;
 <n:μ_o(<s-at:LABCONST>
 <s-dn:μ_o(<s-env:ENV>
 <s-c:
 μ(outerct;<SUCC₁*tn(outerct):
 exit(outerenv);
 int-st-list(attr)>>>)
 for: is-n(n), is-attr(attr), is-c(outerct), is-env(outerenv)

Note: tn(control-tree) is the composite selector selecting, in this case, the unique terminal node of control-tree. Thus, the mutation of outerct above has the effect of appending the sub control tree

exit(outerenv);
int-st-list(attr)

to the terminal node end of outerct, thus creating a control tree which executes from the labelled statement on. Tn is defined formally in section 2.

(17) **int-st-list(t) =**
 is-<>(t)-null
 T→
 int-st-list(tail(t));
 int-st(head(t))
 for: is-st-list(t)

(18) **int-st(t) =**
 is-assign-st(t)-int-assign-st(t)
 is-cond-st(t)-int-cond-st(t)
 is-proc-call(t)&(at_i = PROC)-int-proc-call(t)

- $\text{is-block}(t) \rightarrow \text{int-block}(t)$
 $\text{is-goto-st}(t) \& (at_1 = \text{LABCONST } v, at_2 = \text{LABVAR}) \rightarrow \text{int-goto-st}(t)$
 $\text{is-while-st}(t) \rightarrow \text{int-while-st}(t)$
 where: $at_1 = s\text{-at}((s\text{-id}(t)(\text{ENV})(\text{STG})),$
 $at_2 = s\text{-at}((t)(\text{ENV})(\text{STG}))$
 for: $\text{is-st}(t)$
- (I9) $\text{int-assign-st}(t) =$
 $\text{is-var-attr}(s\text{-at} \cdot n_t(\text{STG})) \rightarrow$
 $\text{assign}(n_t, v);$
 $v: \text{int-expr}(s\text{-right-part}(t))$
 $T\text{-error}$
 where: $n_t = (s\text{-left-part}(t))(\text{ENV})$
 for: $\text{is-assign-st}(t)$
- (I10) $\text{assign}(n, v) =$
 $s\text{-stg}; \mu(\text{STG}; \langle s\text{-dn} \cdot n: \text{convert}(v, s\text{-at} \cdot n(\text{STG})) \rangle)$
 for: $\text{is-n}(n), \text{is-value}(v)$
- (I11) $\text{int-cond-st}(t) =$
 $\text{branch}(v, s\text{-then-st}(t), s\text{-else-st}(t));$
 $v: \text{int-expr}(s\text{-expr}(t))$
 for: $\text{is-cond-st}(t)$
- (I12) $\text{branch}(v, st1, st2) =$
 $\text{convert}(v, \text{LOG}) \rightarrow \text{int-st}(st1)$
 $\rightarrow \text{convert}(v, \text{LOG}) \rightarrow \text{int-st}(st2)$
 for: $\text{is-value}(v), \text{is-st}(st1), \text{is-st}(st2)$
- (I13) $\text{int-proc-call}(t) =$
 $(\text{length}(\text{arg-list}_t) = \text{length}(\text{p-list}_t)) \rightarrow$
 $\text{exit}(\text{ENV});$
 $\text{int-st}(st_t);$
 $\text{establish-env}(\text{env}_t, \text{p-list}_t, \text{arg-list}_t)$
 where: $n_t = (s\text{-id}(t))(\text{ENV}),$
 $\text{p-list}_t = s\text{-param-list} \cdot s\text{-attr} \cdot s\text{-dn} \cdot n_t(\text{STG}),$
 $\text{env}_t = s\text{-env} \cdot s\text{-dn} \cdot n_t(\text{STG}),$
 $\text{arg-list}_t = s\text{-arg-list}(t),$
 $st_t = s\text{-st} \cdot s\text{-attr} \cdot s\text{-dn} \cdot n_t(\text{STG})$
 for: $\text{is-proc-call}(t)$
- (I13') $\text{establish-env}(\text{env}, \text{p-list}, \text{arg-list}) =$
 $s\text{-env}; \mu(\text{env}; \{ \langle \text{elem}(i, \text{p-list}): \text{elem}(i, \text{arg-list})(\text{ENV}) \rangle \mid$
 $1 \leq i \leq \text{length}(\text{p-list}) \})$
 for: $\text{is-env}(\text{env}), \text{is-id-list}(\text{p-list}), \text{is-id-list}(\text{arg-list})$
- (I14) $\text{exit}(\text{env}) =$
 $s\text{-env}: \text{env}$
 for: $\text{is-env}(\text{env})$
- (I15) $\text{int-expr}(t) =$
 $\text{is-bin}(t) \rightarrow$

```

    int-bin-op(s-op(t),a,b);
    a:int-expr(s-rd2(t));
    b:int-expr(s-rd1(t))
is-unary(t)→
    int-un-op(s-op(t),a);
    a:int-expr(s-rd(t))
is-funct-des(t)&(att = FUNCT)→
    int-funct-call(t)
is-var(t)&is-var-attr(s-at * nt(STG))→
    PASS:s-dn * nt(STG)
is-const(t)→
    PASS:value(t)

```

T-error

where: n_t = t(ENV),
 at_t = s-at * (s-id(t)(ENV))(STG)
 for: is-expr(t)

(I16) pass-back(v) = PASS:v
 for: is-value(v)

(I17) int-funct-call(t) =
 (length(arg-list_t) = length(p-list_t))→
 pass-back(v);
 exit(ENV);
 v:int-expr(expr_t);
 int-st(st_t);
 establish-env(env_t,p-list_t,arg-list_t)

where: n_t = (s-id(t))(ENV),
 p-list_t = s-param-list * s-attr * s-dn * n_t(STG),
 env_t = s-env * s-dn * n_t(STG),
 arg-list_t = s-arg-list(t),
 st_t = s-st * s-attr * s-dn * n_t(STG),
 expr_t = s-expr * s-attr * s-dn * n_t(STG)
 for: is-function-des(t)

(I18) int-goto-st(t) =
 s-env:env_t,
 s-c:c_t
 where: dn_t = s-dn * (t(ENV))(STG),
 env_t = s-env(dn_t),
 c_t = s-c(dn_t)
 for: is-goto-st(t)

(I19) int-while-st(t) =
 loop-or-exit(v,t);
 v:int-expr(s-cond(t))
 for: is-while-st(t)

(I20) loop-or-exit(v,t) =
 convert(v,LOG)→
 int-while-st(t);
 int-st(s-body(t))

T-null
for: is-value(v), is-while-st(t)

where (defined):

un-name =
 PASS:n
where: $n(\text{STG}) = \Omega$

null =
 PASS: Ω

where:

The following functions and instructions are not further specified:

convert(v,attr) function which yields v converted (if necessary) to the type specified by attr which may either be INT or LOG.

int-bin-op(op,a,b) Instruction which returns the result of applying the operator op to a and b. It is left open whether there is a conversion performed in case the operator is not applicable to operands of type a and b.

int-un-op(op,a) Instruction which returns the result of applying the operator op to a (for the problem of conversion see above).

value(a) Function which yields the value given a constant a.

Appendix III

Abstract Syntax of Program:

- (A1) is-progr = is-block
- (A2) is-block = (<s-decl-part:is-decl-part>, <s-st-list:is-st-list>)
- (A3) is-decl-part = ({<id:is-attr> || is-id(id)})
- (A4) is-attr = is-var-attr ∨ is-proc-attr ∨ is-funct-attr ∨ is-label-attr
- (A5) is-var-attr = {INT, LOG, LABVAR}
- (A6) is-proc-attr = (<s-param-list:is-id-list>, <s-st:is-st>)
- (A7) is-funct-attr = (<s-param-list:is-id-list>, <s-st:is-st>, <s-expr:is-expr>)
- (A7.5) is-label-attr = is-st-list
- (A8) is-st = is-assign-st ∨ is-cond-st ∨ is-proc-call ∨ is-block ∨
is-goto-st ∨ is-while-st ∨ is-par-block
- (A9) is-assign-st = (<s-left-part:is-var>, <s-right-part:is-expr>)
- (A10) is-expr = is-cont ∨ is-var ∨ is-funct-des ∨ is-bin ∨ is-unary
- (A11) is-const = is-log ∨ is-int
- (A12) is-var = is-id
- (A13) is-funct-des = (<s-id:is-id>, <s-arg-list:is-id-list>)
- (A14) is-bin = (<s-rd1:is-expr>, <s-rd2:is-expr>, <s-op:is-bin-rt>)
- (A15) is-unary = (<s-rd:is-expr>, <s-op:is-unary-rt>)
- (A16) is-cond-st = (<s-expr:is-expr>, <s-then-st:is-st>, <s-else-st:is-st>)
- (A17) is-proc-call = (<s-id:is-id>, <s-arg-list:is-id-list>)
- (A18) is-goto-st = id-id
- (A19) is-while-st = (<s-cond:is-expr>, <s-body:is-st>)
- (A20) is-par-block = (<s-par:is-st-list>)

where:

is- \tilde{id}	an infinite set of identifiers
is-log	a set of constants denoting the truth values
is-int	an infinite set of constants denoting the integer values
is-unary-rt	a set of unary (one-place) operators
is-binary-rt	a set of binary (two-place) operators
{INT, LOG}	two attributes used to distinguish integer variables from logical variables.

Abstract Syntax of State:

- (S1) is-state = (<s-cis-c>, <s-stg:is-stg>)
- (S2) is-env = ({<id:is-n> || is-id(id)})
- (S3) is-c = ... (standard control trees as defined in LLS70)
- (S4) is-stg = ({<n:(<s-dn:is-den>, <s-at:is-type>) || is-n(n)>})
- (S5) is-type = {INT, LOG, PROC, FUNCT, LABVAR, LABCONST}
- (S6) is-den = is-proc-den ∨ is-funct-den ∨ is-value ∨
is-label-den ∨ is-UNINIT
- (S6.1) is-proc-den = (<s-env:is-env>, <s-attr:is-proc-attr>)

- (S6.2) $is\text{-}funct\text{-}den = (\langle s\text{-}env, is\text{-}env \rangle, \langle s\text{-}attr, is\text{-}funct\text{-}attr \rangle)$
 (S6.3) $is\text{-}label\text{-}den = (\langle s\text{-}c, is\text{-}c \rangle)$

where:

- $is\text{-}n$ infinite set of names (used for the generation of unique names)
- {PROC, FUNCT} two attributes used to distinguish function names and procedure names
- {s-env, s-c, s-at, s-dn, s-stg} selectors for the components of the interpreting machine
- $is\text{-}value$ infinite set of values, the integers and the truth values

The *initial state* for any given program $t \in is\text{-}progr$ is:
 $\mu_0(\langle s\text{-}c, int\text{-}progr(t) \rangle, \langle s\text{-}n, l \rangle)$

States ξ whose control part $s\text{-}c(\xi)$ is Ω are *end states*.

Abbreviations used in Instruction Schemata:

- C** $s\text{-}c(\xi)$
STG $s\text{-}stg(\xi)$

Instruction Schemata:

- (I1) $int\text{-}progr(t) = int\text{-}block(t, \Omega)$
 for: $is\text{-}progr(t)$
- (I2) $int\text{-}block(t, env) =$
 $exit;$
 $int\text{-}st\text{-}l1st(s\text{-}st\text{-}list(t), env');$
 $int\text{-}decl\text{-}part(s\text{-}decl\text{-}part(t), env');$
 $env': update\text{-}env(s\text{-}decl\text{-}part(t), env)$
 for: $is\text{-}block(t), is\text{-}env(env)$
- (I3) $update\text{-}env(t, env) =$
 $pass\text{-}env(e, env);$
 $\{id(e): un\text{-}name \mid id(t) \neq \Omega\}$
 for: $is\text{-}decl\text{-}part(t), is\text{-}env(env)$
- (I4) $pass\text{-}env(e, env) =$
 $PASS: \mu(env; \{\langle id: n \rangle \mid id(e) \neq \Omega \& id(e) = n\})$
 for: $is\text{-}env(e), is\text{-}env(env)$

- (15) $\text{int-decl-part}(t, \text{env}) =$
 null;
 {int-decl(id(env), id(t), env) | id(t) \neq Ω }
 for: is-decl-part(t), is-env(env)
- (16) $\text{int-decl}(n, \text{attr}, \text{env}) =$
 is-var-attr(attr) \rightarrow
 s-stg: μ (STG;
 <n: μ_0 (<s-at:attr>,
 <s-dn:UNINIT>)>)
 is-proc-attr(attr) \rightarrow
 s-stg: μ (STG;
 <n: μ_0 (<s-at:PROC>,
 <s-dn: μ_0 (<s-attr:attr>,
 <s-env:env>)>)>)
 is-funct-attr(attr) \rightarrow
 s-stg: μ (STG;
 <n: μ_0 (<s-at:FUNCT>,
 <s-dn: μ_0 (<s-attr:attr>,
 <s-env:env>)>)>)
 is-label-attr(attr) \rightarrow
 s-stg: μ (STG;
 <n: μ_0 (<s-at:LABCONST>,
 <s-dn: μ_0
 <s-c:
 μ (C; <pred²(τ (C):
 int-st-llst(attr)>)>)>)>)
 for: is-n(n), is-attr(attr), is-env(env)

Note: τ is the formal parameter of the function $\Phi_{\text{int-decl}}$, which is a composite selector selecting the currently executed instruction in $s-c(\xi)$; when the instruction schema for int-decl is converted into the definition of $\Phi_{\text{int-decl}}$ the τ in the schema ends up being bound by the formal parameter τ in the definition of $\Phi_{\text{int-decl}}$. $\text{Pred}^2(\chi)$ is the composite selector selecting two nodes up from the leaf end of χ . These are defined formally in section 4.

- (17) $\text{int-st-llst}(t, \text{env}) =$
 is-<>(t) \rightarrow null
 T \rightarrow
 int-st-llst(tail(t), env);
 int-st(head(t), env)
 for: is-st-list(t)
- (18) $\text{int-st}(t, \text{env}) =$
 is-assign-st(t) \rightarrow int-assign-st(t, env)
 is-cond-st(t) \rightarrow int-cond-st(t, env)
 is-proc-call(t) & {at₁ = PROC} \rightarrow int-proc-call(t, env)
 is-block(t) \rightarrow int-block(t, env)
 is-goto-st(t) & {at₁ = LABCONST \vee at₁ = LABVAR} \rightarrow int-goto-st(t, env)
 is-while-st(t) \rightarrow int-while-st(t, env)
 where: at₁ = s-at((s-id(t)(env))(STG),
 at₁ = s-at((t(env))(STG)
 for: is-st(t)

- (I9) **int-assign-st**(t,env) =
 is-var-attr(s-at * n_t(STG))→
 assign(n,v);
 v:**int-expr**(s-right-part(t))
 T-error
 where: n_t = (s-left-part(t))(env)
 for: **is-assign-st**(t)
- (I10) **assign**(n,v) =
 s-stg:μ(STG;<s-dn * n:convert(v,s-st * n(STG))>)
 for: **is-n**(n), **is-value**(v)
- (I11) **int-cond-st**(t,env) =
 branch(v,s-then-st(t),s-else-st(t),env);
 v:**int-expr**(s-expr(t))
 for: **is-cond-st**(t), **is-env**(env)
- (I12) **branch**(v,st1,st2,env) =
 convert(v,LOG)→**int-st**(st1,env)
 ¬**convert**(v,LOG)→**int-st**(st2,env)
 for: **is-value**(v), **is-st**(st1), **is-st**(st2), **is-env**(env)
- (I13) **int-proc-call**(t,env) =
 (length(arg-list_t)=length(p-list_t))→
 exit;
 int-st(st_t,env');
 env':**establish-env**(env_t,p-list_t,arg-list_t,env)
 where: n_t = (s-id(t))(env),
 p-list_t = s-param-list * s-attr * s-dn * n_t(STG),
 env_t = s-env * s-dn * n_t(STG),
 st_t = s-st * s-attr * s-dn * n_t(STG),
 arg-list_t = s-arg-list(t)
 for: **is-proc-call**(t)
- (I13) **establish-env**(env_t,p-list_t,arg-list_t,env) =
 PASS:μ(env_t{<elem(i,p-list_t):elem(i,arg-list_t)(env)> |
 1≤i≤length(p-list_t)})
 for: **is-env**(env_t),**is-id-list**(p-list_t),**is-id-list**(arg-list_t),**is-env**(env)
- (I14) **exit** = null
- (I15) **int-expr**(t,env) =
 is-bin(t)→
 int-bin-op(s-op(t),a,b);
 a:**int-expr**(s-rd1(t),env),
 b:**int-expr**(s-rd2(t),env)
 is-unary(t)→
 int-un-op(s-op(t),a);
 a:**int-expr**(s-rd(t),env)
 is-funct-des(t)&(st_t = FUNCT)→
 int-funct-call(t,env)
 is-var(t)&**is-var-attr**(s-at * n_t(STG))→
 PASS:s-dn * n_t(STG)

$is_const(t) \rightarrow PASS: value(t)$
 $T \rightarrow error$
 where: $n_t = t(env)$,
 $at_t = s-at * (s-id(t)(env))(STG)$
 for: $is_expr(t)$, $is_env(env)$

(I16) $pass-back(v) = PASS:v$
 for: $is_value(v)$

(I17) $int-funct-call(t, env) =$
 $(length(arg-list_t) = length(p-list_t)) \rightarrow$
 $pass-back(v);$
 $exit;$
 $v: int-expr(expr_t, env');$
 $int-st(st_t, env');$
 $env: establish-env(env_t, p-list_t, arg-list_t, env)$
 where: $n_t = (s-id(t))(env)$,
 $p-list_t = s-param-list * s-attr * s-dn * n_t(STG)$,
 $env_t = s-env * s-dn * n_t(STG)$,
 $arg-list_t = s-arg-list(t)$,
 $st_t = s-st * s-attr * n_t(STG)$,
 $expr_t = s-expr * s-attr * s-dn * n_t(STG)$
 for: $is_function-des(t)$, $is_env(env)$

(I18) $int-goto-st(t, env) =$
 $s-c: s-c * s-dn * (t(env))(STG)$
 for: $is_goto-st(t)$, $is_env(env)$

(I19) $int-while-st(t, env) =$
 $loop-or-exit(v, t, env);$
 $v: int-expr(s-cond(t), env)$
 for: $is_while-st(t)$, $is_env(env)$

(I20) $loop-or-exit(v, t, env) =$
 $convert(v, LOG) \rightarrow$
 $int-while-st(t, env);$
 $int-st(s-body(t), env)$
 $T \rightarrow null$
 for: $is_value(v)$, $is_while-st(t)$, $is_env(env)$

(I21) $int-par-block(t, env) =$
 $null;$
 $\{int-st(alem(i, s-par(t)), env) \mid 1 \leq i \leq length(s-par(t))\}$
 for: $is_par-block(t)$

where (defined):
 $un-name =$
 $PASS:m$
 where: $n(STG) = \Omega$
 $null =$
 $PASS:\Omega$

where:

The following functions and instructions are not further specified:

convert(v,attr) function which yields v converted (if necessary) to the type specified by attr which may either be INT or LOG.

int-bin-op(op,a,b) Instruction which returns the result of applying the operator op to a and b. It is left open whether there is a conversion performed in case the operator is not applicable to operands of type a and b.

int-un-op(op,a) Instruction which returns the result of applying the operator op to a (for the problem of conversion see above).

value(a) Function which yields the value given a constant a.

Appendix IID

Abstract Syntax of Program:

- (A1) $is-progr = is-block$
- (A2) $is-block = (\langle s-decl-part:is-decl-list \rangle, \langle s-st-list:is-st-list \rangle)$
- (A3) $is-decl = (\langle s-id:is-id \rangle, \langle s-attr:is-attr \rangle)$
- (A4) $is-attr = is-var-attr \vee is-proc-attr \vee is-funct-attr \vee is-label-attr$
- (A5) $is-var-attr = \{INT, LOG, LABVAR\}$
- (A6) $is-proc-attr = (\langle s-param-list:is-id-list \rangle, \langle s-st:is-st \rangle)$
- (A7) $is-funct-attr = (\langle s-param-list:is-id-list \rangle, \langle s-st:is-st \rangle, \langle s-expr:is-expr \rangle)$
- (A7.5) $is-label-attr = is-st-list$
- (A8) $is-st = is-assign-st \vee is-cond-st \vee is-proc-call \vee is-block \vee is-goto-st \vee is-while-st$
- (A9) $is-assign-st = (\langle s-left-part:is-var \rangle, \langle s-right-part:is-expr \rangle)$
- (A10) $is-expr = is-cont \vee is-var \vee is-funct-des \vee is-bin \vee is-unary$
- (A11) $is-const = is-log \vee is-int$
- (A12) $is-var = is-id$
- (A13) $is-funct-des = (\langle s-id:is-id \rangle, \langle s-arg-list:is-id-list \rangle)$
- (A14) $is-bin = (\langle s-rd1:is-expr \rangle, \langle s-rd2:is-expr \rangle, \langle s-op:is-bin-rt \rangle)$
- (A15) $is-unary = (\langle s-rd:is-expr \rangle, \langle s-op:is-unary-rt \rangle)$
- (A16) $is-cond-st = (\langle s-expr:is-expr \rangle, \langle s-then-st:is-st \rangle, \langle s-else-st:is-st \rangle)$
- (A17) $is-proc-call = (\langle s-id:is-id \rangle, \langle s-arg-list:is-id-list \rangle)$
- (A18) $is-goto-st = id-id$
- (A19) $is-while-st = (\langle s-cond:is-expr \rangle, \langle s-body:is-st \rangle)$

where:

- $is-\hat{id}$ an infinite set of identifiers
- $is-log$ a set of constants denoting the truth values
- $is-\hat{int}$ an infinite set of constants denoting the integer values
- $is-unary-rt$ a set of unary (one-place) operators
- $is-binary-rt$ a set of binary (two-place) operators
- $\{INT, LOG\}$ two attributes used to distinguish integer variables from logical variables.

Abstract Syntax of State Components:

- (S2) $is-env = (\{\langle id:is-n \rangle \parallel is-id(id)\})$
- (S4) $is-stg = (\{\langle n:(\langle s-dn:is-den \rangle, \langle s-at:is-type \rangle) \parallel is-n(n) \rangle\})$
- (S5) $is-type = \{INT, LOG, PROC, FUNCT, LABVAR, LABCONST\}$
- (S6) $is-den = is-proc-den \vee is-funct-den \vee is-value \vee is-label-den \vee is-UNINIT$
- (S6.1) $is-proc-den = (\langle s-env:is-env \rangle, \langle s-attr:is-proc-attr \rangle)$
- (S6.2) $is-funct-den = (\langle s-env:is-env \rangle, \langle s-attr:is-funct-attr \rangle)$
- (S6.3) $is-label-den = (\langle s-env:is-env \rangle, \langle s-c:is-in-Cont \rangle)$
 $is-in-Cont(p) = p \in Cont$

where:

$is\hat{-}n$	infinite set of names (used for the generation of unique names)
{PROC, FUNCT}	two attributes used to distinguish function names and procedure names
{s-env,s-c, s-at,s-dn, s-stg}	selectors for the components of the interpreting machine

$is\hat{-}value$ infinite set of values, the integers and the truth values

Note that predicates of the form

$$is\hat{-}t = \{ \langle d:is\hat{-}r \rangle \mid is\hat{-}d(d) \}$$

describe tables, i.e., functions on $is\hat{-}d$ to $is\hat{-}r$. In fact, For all t such that $is\hat{-}t(t)$, There exists a unique $f \in [is\hat{-}d \rightarrow is\hat{-}r]$ such that for all d such that $is\hat{-}d(d)$,

$$d(t) = f(d);$$

f is said to be the function represented by the table t (f is unique since for all d' not appearing explicitly in t , $d'(t)$ is taken as Ω). In addition,

$$\mu(t; \langle d:r \rangle)$$

represents the function

$$f[d/r].$$

Thus, the elements of

$$is\hat{-}env$$

represent the elements of

$$[is\hat{-}id \rightarrow is\hat{-}n],$$

and the elements of

$$is\hat{-}stg$$

represent the elements of

$$[is\hat{-}n \rightarrow \{ \langle s\hat{-}dn:is\hat{-}den \rangle, \langle s\hat{-}at:is\hat{-}type \rangle \}].$$

In denotational semantics, function application is denoted by juxtaposition of the function name with its arguments. In VDL, function application is denoted by the function name followed by a parenthesized argument list. When calculating values of arguments to the denotational meaning function and to a continuation, the VDL convention is used, but when applying the denotational meaning function or a continuation, the denotational semantics convention is used.

Semantic Equations:

In the following,

$is\hat{-}env(ENV)$, $is\hat{-}in\text{-}Cont(p)$, $is\hat{-}stg(STG)$,
 $is\hat{-}env(env)$, $is\hat{-}stg(stg)$, $is\hat{-}value(v)$, and likewise
 for any primed version of these symbols.

$$(I1) \quad Mean \llbracket int\text{-}progr(t) \rrbracket ENV \wp STG = \\ Mean \llbracket int\text{-}block(t) \rrbracket ENV \wp STG \\ \text{for: } is\hat{-}progr(t)$$

$$(I2) \quad Mean \llbracket int\text{-}block(t) \rrbracket ENV \wp STG = \\ Mean \llbracket update\text{-}env(s\text{-}decl\text{-}part(t)) \rrbracket ENV \wp' STG \\ \text{where } \wp' \vee env \ stg =$$

- Mean* $[[dsle(s-decl-part(t),s-st-list(t),p,ENV)] env p stg$
for: is-block(t)
- (I2') *Mean* $[[dsle(dp,sl,outerquf,outerenv)] ENV p STG =$
Mean $[[int-decl-part(dp,outerquf,outerenv)] ENV p' STG$
where $p' v env stg =$
Mean $[[sle(sl,outerenv)] env p stg$
for: is-decl-part(dp), is-st-list(sl), is-in-Cont(outerquf), is-env(outerenv)
- (I2'') *Mean* $[[sle(sl,outerenv)] ENV p STG =$
Mean $[[int-st-list(sl)] ENV p' STG$
where $p' v env stg =$
Mean $[[exit(outerenv)] env p stg$
for: is-st-list(sl), is-env(outerenv)
- (I3) *Mean* $[[update-env(t)] ENV p STG =$
 $is-<>(t)-Mean$ $[[null] ENV p STG$
 $\top-Mean$ $[[un-name] ENV p' STG$
where $p' v env stg =$
Mean $[[le(s-id(head(t)),v,tail(t))] env p stg$
for: is-decl-part(t)
- (I3') *Mean* $[[le(is,n,dp)] ENV p STG =$
Mean $[[update-id(id,n)] ENV p' STG$
where $p' v env stg =$
Mean $[[update-env(dp)] env p stg$
for: is-id(id), is-n(n), is-decl-part(dp)
- (I4) *Mean* $[[update-id(id,n)] ENV p STG =$
 $p \Omega \mu(ENV; <id:n>) STG$
for: is-id(id), is-n(n)
- (I5) *Mean* $[[int-decl-part(t,outerquf,outerenv)] ENV p STG =$
 $is-<>(t)-Mean$ $[[null] ENV p STG$
 $\top-$
Mean $[[int-decl(s-id(head(t))(ENV),s-attr(head(t)),outerquf,$
 $outerenv)] ENV p' STG$
where: $p' v env stg =$
Mean $[[int-decl-part(tail(t),outerquf,outerenv)] env p stg$
for: is-decl-part(t), is-in-Cont(outerquf), is-env(outerenv)
- (I6) *Mean* $[[int-decl(n,attr,outerquf,outerenv)] ENV p STG =$
 $is-var-attr(attr)-$
 $p \Omega ENV \mu(STG;$
 $\quad \quad \quad <n:\mu_0(<s-st:attr>, <s-dn:UNINIT>)>)$
 $is-proc-attr(attr)-$
 $p \Omega ENV \mu(STG;$
 $\quad \quad \quad <n:\mu_0(<s-st:PROC>, <s-dn:\mu_0(<s-attr:attr>, <s-env:ENV>)>)>)$
 $is-funct-attr(attr)-$

$$p \ \Omega \ \text{ENV} \ \mu(\text{STG};$$

$$\langle n: \mu_0(\langle s\text{-st:FUNCT} \rangle,$$

$$\langle s\text{-dn:} \mu_0(\langle s\text{-attr:attr} \rangle,$$

$$\langle s\text{-env:ENV} \rangle \rangle \rangle \rangle)$$

$$\text{is-label-attr(attr)} \rightarrow$$

$$p \ \Omega \ \text{ENV} \ \mu(\text{STG};$$

$$\langle n: \mu_0(\langle s\text{-st:LABCONST} \rangle,$$

$$\langle s\text{-dn:} \mu_0(\langle s\text{-env:ENV} \rangle,$$

$$\langle s\text{-c:p'} \rangle \rangle \rangle \rangle)$$

where: $p' \ v \ \text{env} \ \text{stg} =$

$\text{Mean} \ [\text{sl}(\text{attr}, \text{outerenv})] \ \text{env} \ \text{outerquf} \ \text{stg}$

for: $\text{is-n}(n)$, $\text{is-attr}(\text{attr})$, $\text{is-in-Cont}(\text{outerquf})$, $\text{is-env}(\text{outerenv})$

(I7) $\text{Mean} \ [\text{Int-st-list}(t)] \ \text{ENV} \ p \ \text{STG} =$
 $\text{is-} \langle \rangle (t) \rightarrow \text{Mean} \ [\text{null}] \ \text{ENV} \ p \ \text{STG}$
 $T \rightarrow$

$\text{Mean} \ [\text{Int-st}(\text{head}(t))] \ \text{ENV} \ p' \ \text{STG}$

where: $p' \ v \ \text{env} \ \text{stg} =$

$\text{Mean} \ [\text{Int-st-list}(\text{tail}(t))] \ \text{env} \ p \ \text{stg}$

for: $\text{is-st-list}(t)$

(I8) $\text{Mean} \ [\text{Int-st}(t)] \ \text{ENV} \ p \ \text{STG} =$
 $\text{is-assign-st}(t) \rightarrow \text{Mean} \ [\text{Int-assign-st}(t)] \ \text{ENV} \ p \ \text{STG}$
 $\text{is-cond-st}(t) \rightarrow \text{Mean} \ [\text{Int-cond-st}(t)] \ \text{ENV} \ p \ \text{STG}$
 $\text{is-proc-call}(t) \ \& \ (at_1 = \text{PROC}) \rightarrow$
 $\text{Mean} \ [\text{Int-proc-call}(t)] \ \text{ENV} \ p \ \text{STG}$
 $\text{is-block}(t) \rightarrow \text{Mean} \ [\text{Int-block}(t)] \ \text{ENV} \ p \ \text{STG}$
 $\text{is-goto-st}(t) \ \& \ (at_1 = \text{LABCONST} \vee at_1 = \text{LABVAR}) \rightarrow$
 $\text{Mean} \ [\text{Int-goto-st}(t)] \ \text{ENV} \ p \ \text{STG}$
 $\text{is-while-st}(t) \rightarrow \text{Mean} \ [\text{Int-while-st}(t)] \ \text{ENV} \ p \ \text{STG}$

where: $at_1 = s\text{-st}((s\text{-id}(t)(\text{ENV}))(\text{STG}))$,

$at_2 = s\text{-st}((t(\text{ENV}))(\text{STG}))$

for: $\text{is-st}(t)$

(I9) $\text{Mean} \ [\text{Int-assign-st}(t)] \ \text{ENV} \ p \ \text{STG} =$
 $\text{is-var-attr}(s\text{-at}^* n_1(\text{STG})) \rightarrow$
 $\text{Mean} \ [\text{Int-expr}(s\text{-right-part}(t))] \ \text{ENV} \ p' \ \text{STG}$

$T \rightarrow \text{error}$

where: $p' \ v \ \text{env} \ \text{stg} =$

$\text{Mean} \ [\text{assign}(n_1, v)] \ \text{env} \ p \ \text{Stg},$

$n_1 = (s\text{-left-part}(t))(\text{ENV})$

for: $\text{is-assign-st}(t)$

(I10) $\text{Mean} \ [\text{assign}(n, v)] \ \text{ENV} \ p \ \text{STG} =$
 $p \ \Omega \ \text{ENV} \ \mu(\text{STG}; \langle s\text{-dn}^* n: \text{convert}(v, s\text{-at}^* n(\text{STG})) \rangle)$
for: $\text{is-n}(n)$, $\text{is-value}(v)$

(I11) $\text{Mean} \ [\text{Int-cond-st}(t)] \ \text{ENV} \ p \ \text{STG} =$
 $\text{Mean} \ [\text{Int-expr}(s\text{-expr}(t))] \ \text{ENV} \ p' \ \text{STG}$
where: $p' \ v \ \text{env} \ \text{stg} =$
 $\text{Mean} \ [\text{branch}(v, s\text{-then-st}(t), s\text{-else-st}(t))] \ \text{env} \ p \ \text{stg}$
for: $\text{is-cond-st}(t)$

- (I12) *Mean* $[[\text{branch}(v, \text{st1}, \text{st2})]] \text{ ENV } \rho \text{ STG} =$
 $\text{convert}(v, \text{LOG}) \rightarrow \text{Mean} [[\text{int-st}(\text{st1})]] \text{ ENV } \rho \text{ STG}$
 $\rightarrow \text{convert}(v, \text{LOG}) \rightarrow \text{Mean} [[\text{int-st}(\text{st2})]] \text{ ENV } \rho \text{ STG}$
for: is-value(v), is-st(st1), is-st(st2)
- (I13) *Mean* $[[\text{int-proc-call}(t)]] \text{ ENV } \rho \text{ STG} =$
 $(\text{length}(\text{arg-list}_t) = \text{length}(\text{p-list}_t)) \rightarrow$
 $\text{Mean} [[\text{establish-env}(\text{env}_t, \text{p-list}_t, \text{arg-list}_t)]] \text{ ENV } \rho' \text{ STG}$
where: $\rho' \vee \text{env stg} =$
 $\text{Mean} [[\text{se}(\text{st}_t, \text{ENV})]] \text{ env } \rho' \text{ Stg},$
 $n_t = (\text{s-id}(t))(\text{ENV}),$
 $\text{p-list}_t = \text{s-param-list} * \text{s-attr} * \text{s-dn} * n_t(\text{STG}),$
 $\text{env}_t = \text{s-env} * \text{s-dn} * n_t(\text{STG}),$
 $\text{arg-list}_t = \text{s-arg-list}(t),$
 $\text{st}_t = \text{s-st} * \text{s-attr} * \text{s-dn} * n_t(\text{STG})$
for: is-proc-call(t)
- (I13') *Mean* $[[\text{establish-env}(\text{env}, \text{p-list}, \text{arg-list})]] \text{ ENV } \rho \text{ STG} =$
 $\rho \ \Omega \ \mu(\text{env}; \{ \langle \text{elem}(i, \text{p-list}): \text{elem}(i, \text{arg-list})(\text{ENV}) \rangle \mid$
 $1 \leq i \leq \text{length}(\text{p-list}) \}) \text{ STG}$
for: is-env(env), is-id-list(p-list), is-id-list(arg-list)
- (I13'') *Mean* $[[\text{se}(\text{st}, \text{outerenv})]] \text{ ENV } \rho \text{ STG} =$
 $\text{Mean} [[\text{int-st}(\text{st})]] \text{ ENV } \rho' \text{ STG}$
where: $\rho' \vee \text{env stg} =$
 $\text{Mean} [[\text{exit}(\text{outerenv})]] \text{ env } \rho \text{ stg}$
for: is-st(st), is-env(outerenv)
- (I14) *Mean* $[[\text{exit}(\text{env})]] \text{ ENV } \rho \text{ STG} =$
 $\rho \ \Omega \ \text{env STG}$
for: is-env(env)
- (I15) *Mean* $[[\text{int-expr}(t)]] \text{ ENV } \rho \text{ STG} =$
is-bin(t) \rightarrow
 $\text{Mean} [[\text{int-expr}(\text{s-rd1}(t))]] \text{ ENV } \rho'' \text{ STG}$
is-unary(t) \rightarrow
 $\text{Mean} [[\text{int-expr}(\text{s-rd}(t))]] \text{ ENV } \rho' \text{ STG}$
is-funct-des(t) & (at_t = FUNCT) \rightarrow
 $\text{Mean} [[\text{int-funct-call}(t)]] \text{ ENV } \rho \text{ STG}$
is-var(t) & is-var-attr(s-at * n_t(STG)) \rightarrow
 $\rho \ \text{s-dn} * n_t(\text{STG}) \text{ ENV STG}$
is-const(t) \rightarrow
 $\rho \ \text{value}(t) \text{ ENV STG}$
T-error
where: $\rho'' \ \text{a env stg} =$
 $\text{Mean} [[\text{eb}(\text{s-rd2}(t), \text{s-op}(t), \text{a})]] \text{ env } \rho \text{ Stg},$
 $\rho' \ \text{a env stg} =$
 $\text{Mean} [[\text{int-un-op}(\text{s-op}(t), \text{a})]] \text{ env } \rho \text{ Stg},$
 $n_t = t(\text{ENV}),$
 $\text{at}_t = \text{s-at} * (\text{s-id}(t)(\text{ENV}))(\text{STG})$
for: is-expr(t)

- (I15') *Mean* $[[\text{ob}(\text{rd2}, \text{op}, \text{a})]] \text{ ENV } \rho \text{ STG} =$
Mean $[[\text{int-expr}(\text{rd2})]] \text{ ENV } \rho' \text{ STG}$
 where: $\rho' \text{ b env stg} =$
Mean $[[\text{int-bin-op}(\text{op}, \text{a}, \text{b})]] \text{ env } \rho \text{ stg}$
 for: *is-expr*(rd2), *is-bin-op*(op), *is-value*(a)
- (I16) *Mean* $[[\text{pass-back}(v)]] \text{ ENV } \rho \text{ STG} =$
 $\rho \text{ v ENV STG}$
 for: *is-value*(v)
- (I17) *Mean* $[[\text{int-funct-call}(t)]] \text{ ENV } \rho \text{ STG} =$
 $(\text{length}(\text{arg-list}_t) = \text{length}(\rho\text{-list}_t)) \rightarrow$
Mean $[[\text{establish-env}(\text{env}_t, \rho\text{-list}_t, \text{arg-list}_t)]] \text{ ENV } \rho' \text{ STG}$
 where: $\rho' \text{ v env stg} =$
Mean $[[\text{seep}(\text{st}_t, \text{expr}_t, \text{ENV})]] \text{ env } \rho \text{ Stg}$,
 $n_t = (\text{s-id}(t))(\text{ENV})$,
 $\rho\text{-list}_t = \text{s-param-list} * \text{s-attr} * \text{s-dn} * n_t(\text{STG})$,
 $\text{env}_t = \text{s-env} * \text{s-dn} * n_t(\text{STG})$,
 $\text{arg-list}_t = \text{s-arg-list}(t)$,
 $\text{st}_t = \text{s-st} * \text{s-attr} * \text{s-dn} * n_t(\text{STG})$,
 $\text{expr}_t = \text{s-expr} * \text{s-attr} * \text{s-dn} * n_t(\text{STG})$
 for: *is-function-des*(t)
- (I17') *Mean* $[[\text{seep}(\text{st}, \text{expr}, \text{outerenv})]] \text{ ENV } \rho \text{ STG} =$
Mean $[[\text{int-st}(\text{st})]] \text{ ENV } \rho' \text{ STG}$
 where: $\rho' \text{ v env stg} =$
Mean $[[\text{seep}(\text{expr}, \text{outerenv})]] \text{ env } \rho \text{ stg}$
 for: *is-st*(st), *is-expr*(expr), *is-env*(outerenv)
- (I17'') *Mean* $[[\text{seep}(\text{expr}, \text{outerenv})]] \text{ ENV } \rho \text{ STG} =$
Mean $[[\text{int-expr}(\text{expr})]] \text{ ENV } \rho' \text{ STG}$
 where: $\rho' \text{ v env stg} =$
Mean $[[\text{seep}(\text{outerenv}, v)]] \text{ env } \rho \text{ stg}$
 for: *is-expr*(expr), *is-env*(outerenv)
- (I17''') *Mean* $[[\text{seep}(\text{outerenv}, \text{val})]] \text{ ENV } \rho \text{ STG} =$
Mean $[[\text{exit}(\text{outerenv})]] \text{ ENV } \rho' \text{ STG}$
 where: $\rho' \text{ v env stg} =$
Mean $[[\text{pass-back}(\text{val})]] \text{ env } \rho \text{ stg}$
 for: *is-env*(outerenv), *is-value*(val)
- (I18) *Mean* $[[\text{int-goto-st}(t)]] \text{ ENV } \rho \text{ STG} =$
 $c_t \Omega \text{ env}_t \text{ STG}$
 where: $\text{dn}_t = \text{s-dn} * (t(\text{ENV}))(\text{STG})$,
 $\text{env}_t = \text{s-env}(\text{dn}_t)$,
 $c_t = \text{s-c}(\text{dn}_t)$
 for: *is-goto-st*(t)
- (I19) *Mean* $[[\text{int-while-st}(t)]] \text{ ENV } \rho \text{ STG} =$
Mean $[[\text{int-expr}(\text{s-cond}(t))]] \text{ ENV } \rho' \text{ STG}$
 where: $\rho' \text{ v env stg} =$
Mean $[[\text{loop-or-exit}(v, t)]] \text{ env } \rho \text{ stg}$

for: is-while-st(t)
 (I20) *Mean* **[[loop-or-exit(v,t)] ENV p STG =**
 convert(v,LOG)-
 Mean **[[int-st(s-body(t))] ENV p' STG**
 T-Mean **[[null] ENV p STG**
 where: p' val env stg =
 Mean **[[int-while-st(t)] env p stg**
 for: is-value(v), is-while-st(t)

where (defined):

Mean **[[un-name] ENV p STG =**
 p n ENV STG
 where: n(STG) = Ω

Mean **[[null] ENV p STG =**
 p Ω ENV STG

Mean **[[int-bin-op(op,a,b)] ENV p STG =**
 p (a op b) ENV STG

where: (a op b) is the result of applying the operator op to a and b.
 It is left open whether there is a conversion performed in case
 the operator is not applicable to operands of type a and b.
 for: is-binary-rt(op), is-value(a), is-value(b)

Mean **[[int-un-op(op,a)] ENV p STG =**
 p (op a) ENV STG

where: (op a) is the result of applying the operator op to a.
 It is left open whether there is a conversion performed in case
 the operator is not applicable to an operand of type a.
 for: is-unary-rt(op), is-value(a)

where:

The following functions and instructions are not further specified:

convert(v,attr) function which yields v converted (if necessary) to
 the type specified by attr which may either be INT
 or LOG.

value(a) Function which yields the value given a constant a.

Appendix IV

Syntactic Domains:

- (A1) $\text{is-Exp} = \text{is-0} \vee \text{is-1} \vee \text{is-neg} \vee \text{is-not} \vee \text{is-add} \vee \text{is-equal} \vee$
 $\text{is-Ide} \vee \text{is-procedure}$
- (A2) $\text{is-neg} = \langle \text{s-neg:is-Exp} \rangle$
- (A3) $\text{is-not} = \langle \text{s-not:is-Exp} \rangle$
- (A4) $\text{is-add} = \langle \text{s-add1:is-Exp}, \text{s-add2:is-Exp} \rangle$
- (A5) $\text{is-equal} = \langle \text{s-equal1:is-Exp}, \text{s-equal2:is-Exp} \rangle$
- (A6) $\text{is-Ide} = \dots$
- (A7) $\text{is-procedure} = \langle \text{s-procedure:is-Com} \rangle$
- (A8) $\text{is-Def} = \text{is-var-def} \vee \text{is-const-def}$
- (A9) $\text{is-var-def} = \langle \text{s-new:is-Ide}, \text{s-init:is-Exp} \rangle$
- (A10) $\text{is-const-def} = \langle \text{s-val:is-Ide}, \text{s-init:is-Exp} \rangle$
- (A11) $\text{is-Com} = \text{is-NULL} \vee \text{is-assign} \vee \text{is-call} \vee \text{is-semicolon} \vee \text{is-if} \vee$
 $\text{is-while} \vee \text{is-with} \vee \text{is-labeled-Com} \vee \text{is-Seq}$
- (A12) $\text{is-assign} = \langle \text{s-lp:is-Ide}, \text{s-rp:is-Exp} \rangle$
- (A13) $\text{is-call} = \langle \text{s-call:is-Exp} \rangle$
- (A14) $\text{is-semicolon} = \langle \text{s-first:is-Com}, \text{s-second:is-Com} \rangle$
- (A15) $\text{is-if} = \langle \text{s-if:is-Exp}, \text{s-then:is-Com}, \text{s-else:is-Com} \rangle$
- (A16) $\text{is-while} = \langle \text{s-while:is-Exp}, \text{s-do:is-Com} \rangle$
- (A17) $\text{is-with} = \langle \text{s-with:is-Def}, \text{s-do:is-Com} \rangle$
- (A18) $\text{is-labeled-Com} = \langle \text{s-label:is-Ide}, \text{s-com:is-Com} \rangle$
- (A19) $\text{is-Seq} = \langle \text{s-goto:is-Ide} \rangle$
- (A20) $\text{is-Pro} = \langle \text{s-output:is-Ide}, \text{s-program:is-Com} \rangle$

Semantic Domains:

- (S1) $\text{is}^{\wedge}\text{T} = \{\text{TRUE}, \text{FALSE}\}$
- (S2) $\text{is}^{\wedge}\text{Z} = \{\dots, -2, -1, 0, 1, 2, \dots\}$
- (S3) $\text{is-B} = \text{is-T} \vee \text{is-B}$
- (S4) $\text{is-R} = \text{is-B} \vee \text{is-P}$
- (S5) $\text{is-L} = \dots$ (locations)
- (S6) $\text{is-S} = (\{\langle \text{l:is-R} \vee \text{is-UNUSED} \rangle \mid \text{is-L}(l)\})$
- (S7) $\text{is-D} = \text{is-L} \vee \text{is-R} \vee \text{is-c} \vee \text{is-UNDEFINED}$
- (S8) $\text{is-U} = (\{\langle \text{l:is-D} \rangle \mid \text{is-Ide}(l)\})$
- (S9) $\text{is-P} = \text{is-c}$
- (S10) $\text{is-state} = \langle \text{s-U:is-U}, \text{s-S:is-S}, \text{s-c:is-c} \rangle$
- (S11) $\text{is-c} = \dots$ (control trees)
- (S12) $\text{is-initial-state} = \langle \text{s-U:is-initial-U}, \text{s-S:is-initial-S},$
 $\langle \text{s-c:int-Pro}(P, B) \rangle$
 where: $\text{is-Pro}(P)$, $\text{is-B}(B)$
- (S13) $\text{is-initial-U}(\{\langle \text{l:is-UNDEFINED} \rangle \mid \text{is-Ide}(l)\})$

(S14) $is_initial-S(\{<is-UNUSED>[is-L(I)]\})$

Abbreviations used in Instruction Schemata:

u s-U(ξ)
s s-S(ξ)
c s-C(ξ)

Instruction Schemata:

- (I1) $Int-Exp(E) =$
 $is-0(E) \rightarrow$ PASS:0
 $is-1(E) \rightarrow$ PASS:1
 $is-neg(E) \rightarrow$
test-r-is-Z-and-negate(r);
r:int-Exp(s-neg(E))
 $is-not(E) \rightarrow$
test-r-is-T-and-not(r);
r:int-Exp(s-not(E))
 $is-add(E) \rightarrow$
test-r2-is-Z-and-add(r_1, r_2);
 r_2 :test-r1-is-Z-and-int-Exp($r_1, s-add2(E)$);
 r_1 :int-Exp(s-add1(E))
 $is-equal(E) \rightarrow$
test-r2-is-T-and-equal(r_1, r_2);
 r_2 :test-r1-is-T-and-int-Exp($r_1, s-equal2(E)$);
 r_1 :int-Exp(s-equal1(E))
 $is-Idc(E) \rightarrow$
 $is-L(E(u)) \rightarrow$ PASS:(E(u))(s)
 $is-R(E(u)) \rightarrow$ PASS:E(u)
 $is-procedure(E) \rightarrow$
PASS:int-Com(s-procedure(E));
establish-env(u)
for: is-Exp(E)
- (I2) $test-r-is-Z-and-negate(r) =$
 $is-Z(r) \rightarrow$ PASS: $\neg r$
T-error
for: is-R(r)
- (I3) $test-r-is-T-and-not(r) =$
 $is-T(r) \rightarrow$ PASS: $\neg r$
T-error
for: is-R(r)
- (I4) $test-r2-is-Z-and-add(r_1, r_2) =$
 $is-Z(r_2) \rightarrow$ PASS: $r_1 + r_2$
T-error

- for: is-R(r_1), is-R(r_2)
- (I5) test-r1-is-Z-and-int-Exp(r_1, E) =
 is-Z(r_1)-Int-Exp(E)
 T-error
 for: is-R(r_1), is-Exp(E)
- (I6) test-r2-is-T-and-equal(r_1, r_2) =
 is-T(r_2)-PASS: $r_1=r_2$
 T-error
 for: is-R(r_1), is-R(r_2)
- (I7) test-r1-is-T-and-int-Exp(r_1, E) =
 is-T(r_1)-Int-Exp(E)
 T-error
 for: is-R(r_1), is-Exp(E)
- (I8) Int-Def(D) =
 is-var-def(D)-
 alloc-alloc(s -new(E), r);
 r:int-Exp(s -init(E))
 is-const-def(D)-
 alloc(s -val(E), r);
 r:int-Exp(s -init(E))
 for: is-Def(D)
- (I9) alloc-alloc(I, r) =
 ($\exists I'$)(is-UNUSED($I'(u)$)-
 s-S: $\mu(s; \langle I, r \rangle$)
 s-U: $\mu(u; \langle I, I' \rangle$)
 T-error
 where: is-UNUSED($I(u)$)
 for: is-Ide(I), is-R(r)
- (I10) alloc(I, r) =
 s-U: $\mu(u; \langle I, r \rangle$)
 for: is-Ide(I), is-R(r)
- (I11) Int-Com(C) =
 is-NULL(C)-
 null
 is-assign(C)-
 assign(s -lp(C), r);
 r:int-Exp(s -rp(C))
 is-call(C)-
 call(r);
 r:int-Exp(s -call(C))
 is-semicolon(C)-
 Int-Com(s -second(C));
 Int-Com(s -first(C))
 is-if(C)-
 branch(r, s -then(C), s -else(C));

```

      r:int-Exp(s-if(C))
is-while(C)→
  loop-or-exit(r,C);
  r:int-Exp(s-while(c))
is-with(C)→
  establish-env(u);
  Int-Com(s-do(C));
  Int-Def(s-with(C))
is-labeled-Com(C)→
  establish-env(u);
  Int-Com(s-com(C));
  assoc(s-label(C),c')
where: c' =
      c;
      Int-Com(C)
for: is-Com(C)

```

(I12) null = PASS:Ω

(I13) assign(l,r) =
 is-L(l(u))→s-S:μ(s;<l(u):r>)
 T-error
 for: is-Ide(l), is-R(r)

(I14) call(r) =
 is-P(r)→
 establish-env(u);
 r
 T-error
 for: is-R(r)

(I15) branch(r,C₁,C₂) =
 is-T(r)→
 r→int-Com(C₁)
 T→int-Com(C₂)
 T-error
 for: is-R(r), is-Com(C₁), is-Com(C₂)

Note that the above instruction and the next instruction deviate slightly from Tennent's semantics in that they both check that r is in the domain is-T. This checking is consistent with the rest of the instructions and the rest of Tennent's semantics.

(I16) loop-or-exit(r,C) =
 is-T(r)→
 r→
 int-Com(C);
 int-Com(s-do(C))
 T→
 null
 T-error
 for: is-R(r), is-while(C)

- (I17) **establish-env(U) =**
 s-U:U
for: is-U(U)
- (I18) **Int-Seq(S) =**
 is-c((s-goto(S))(u))-s-c:(s-goto(S))(u)
 T-error
for: is-Seq(S)
- (I19) **Int-Pro(P,B) =**
 establish-env(u);
 Int-Com(s-program(P));
 alloc-assoc(s-output(P),B)
for: is-Pro(P), is-B(B)

